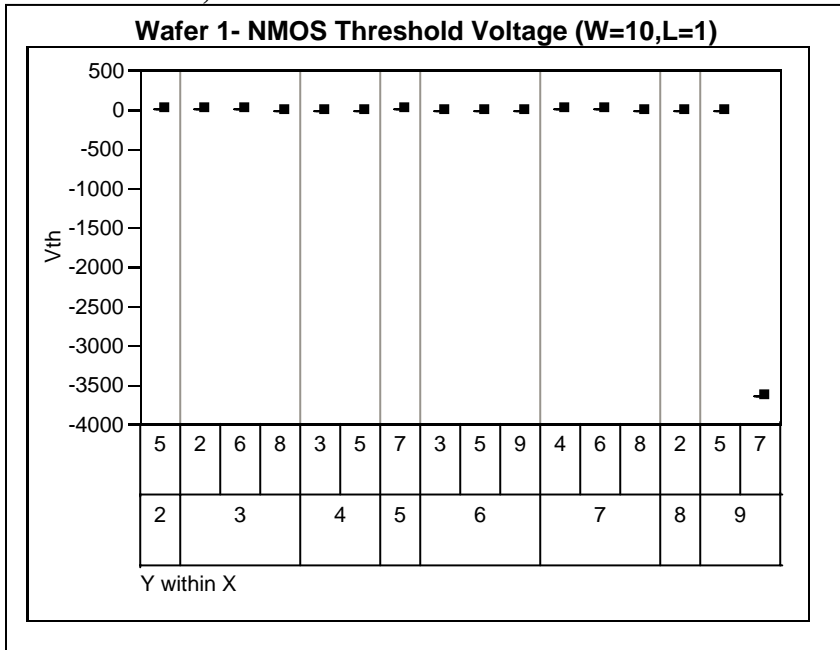
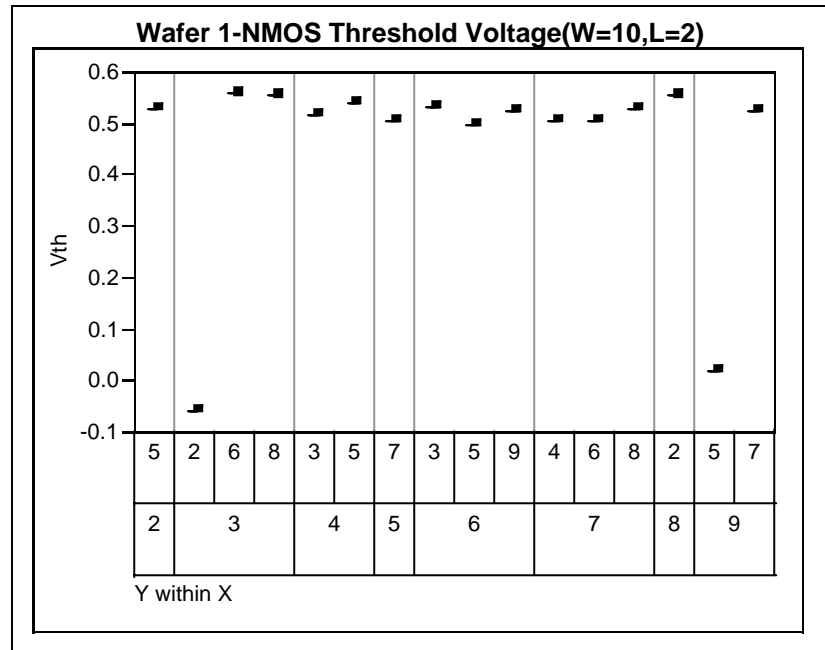
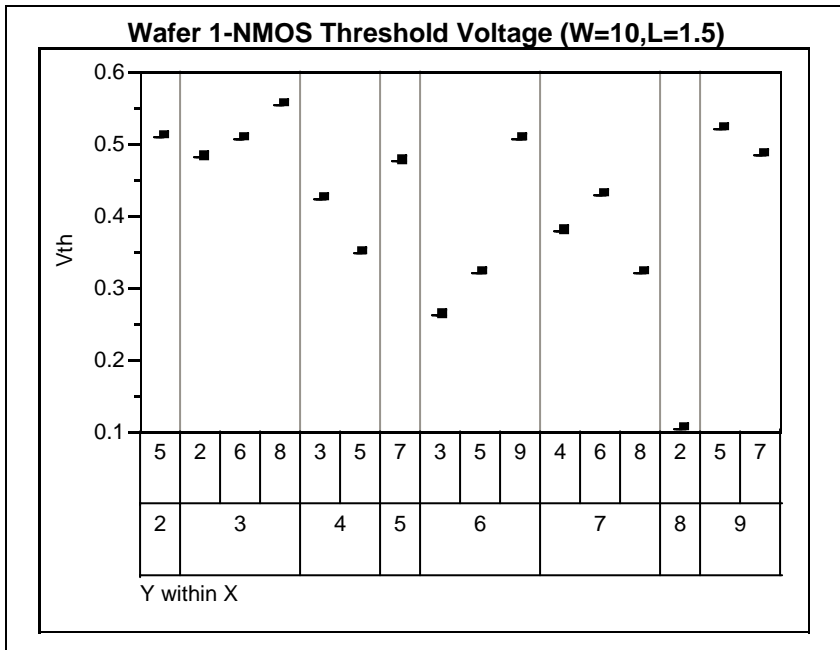
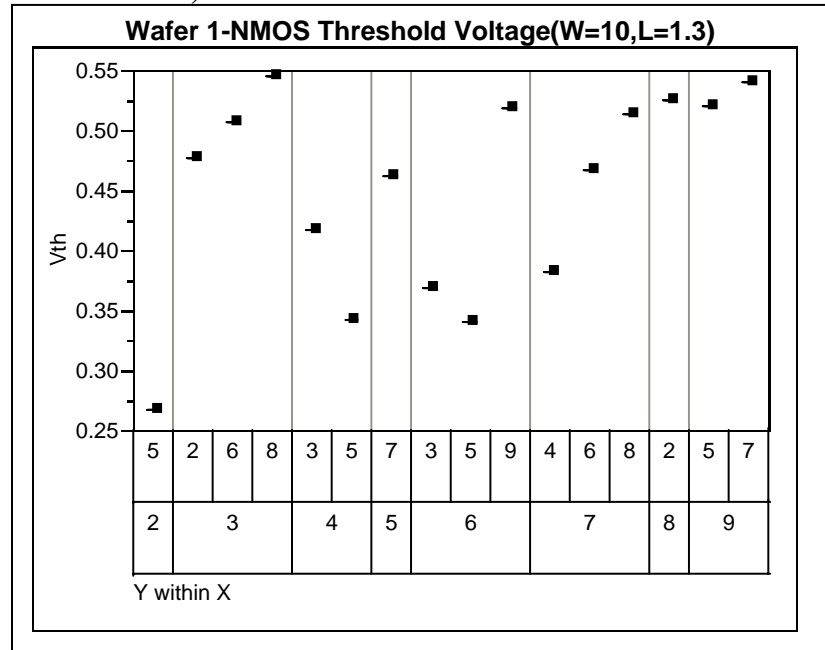


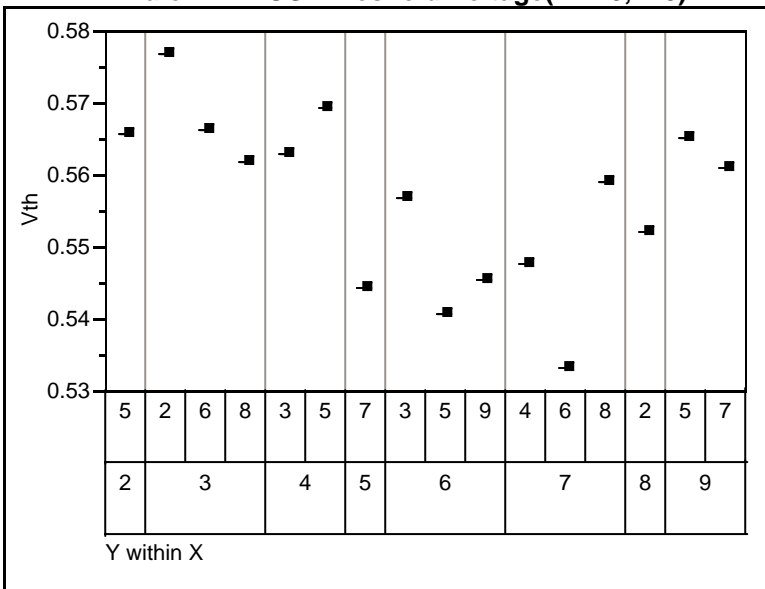
LCL=0,UCL=3V for NMOS



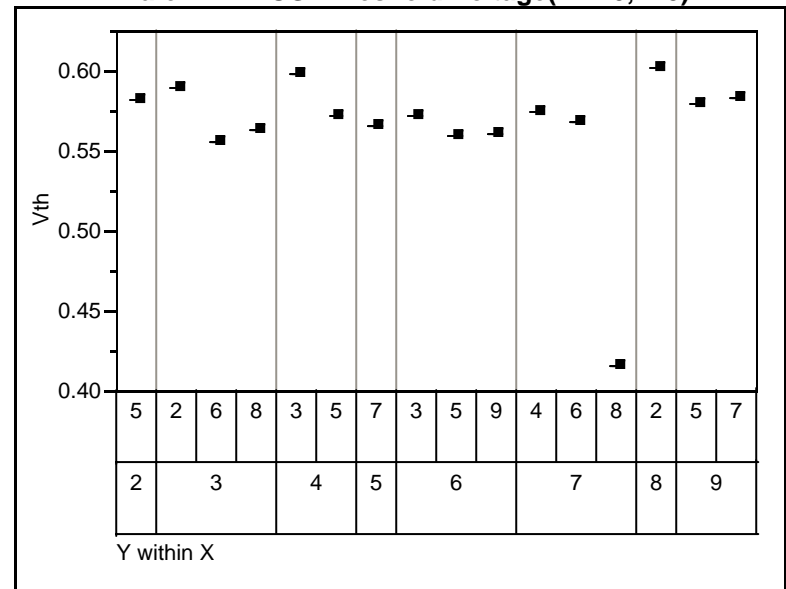
W=10,L=Varied.



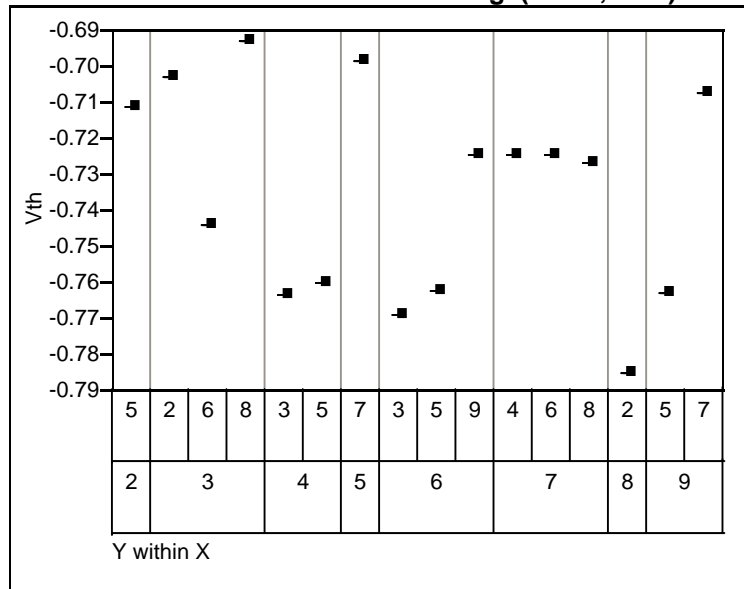
Wafer 1-NMOS Threshold Voltage(W=10,L=3)



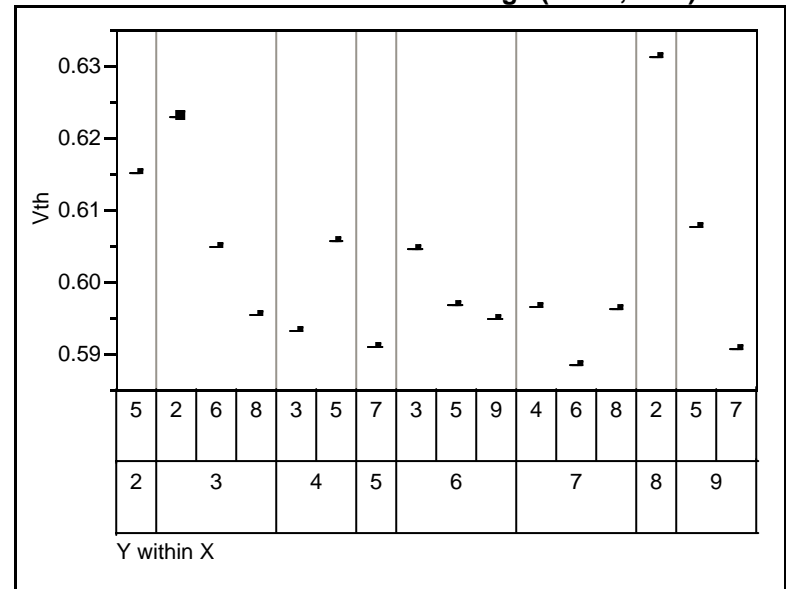
Wafer 1- NMOS Threshold Voltage(W=10,L=5)



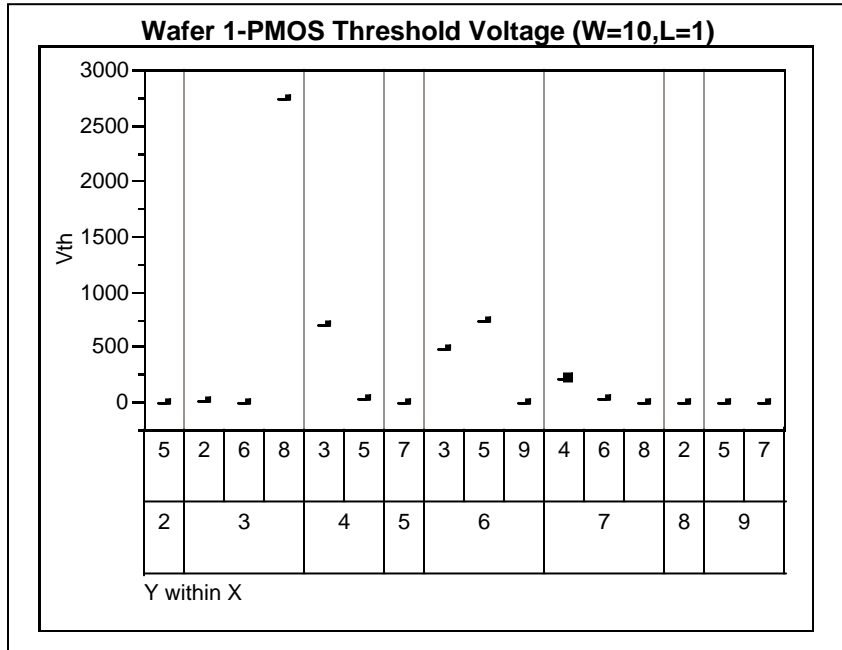
Wafer 1- NMOS Threshold Voltage(W=10,L=10)



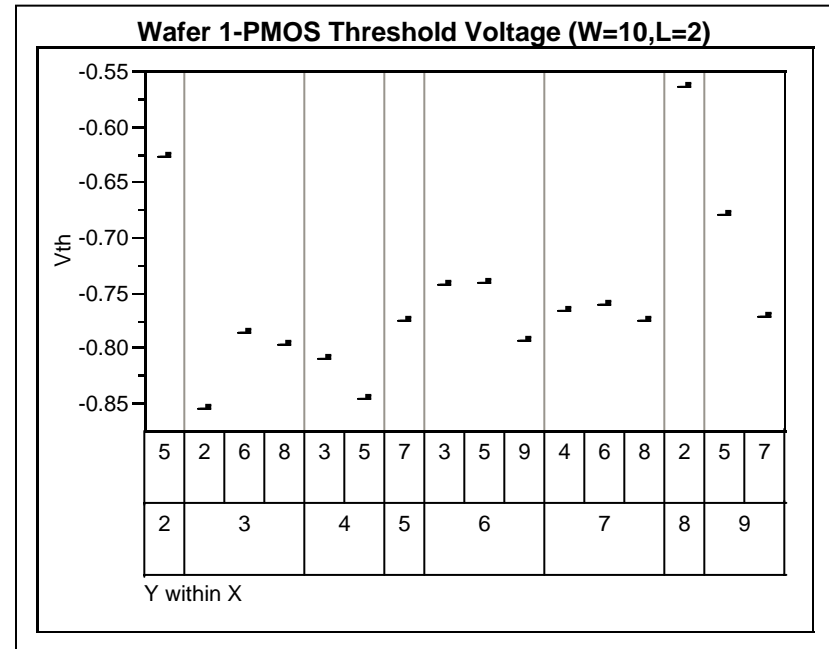
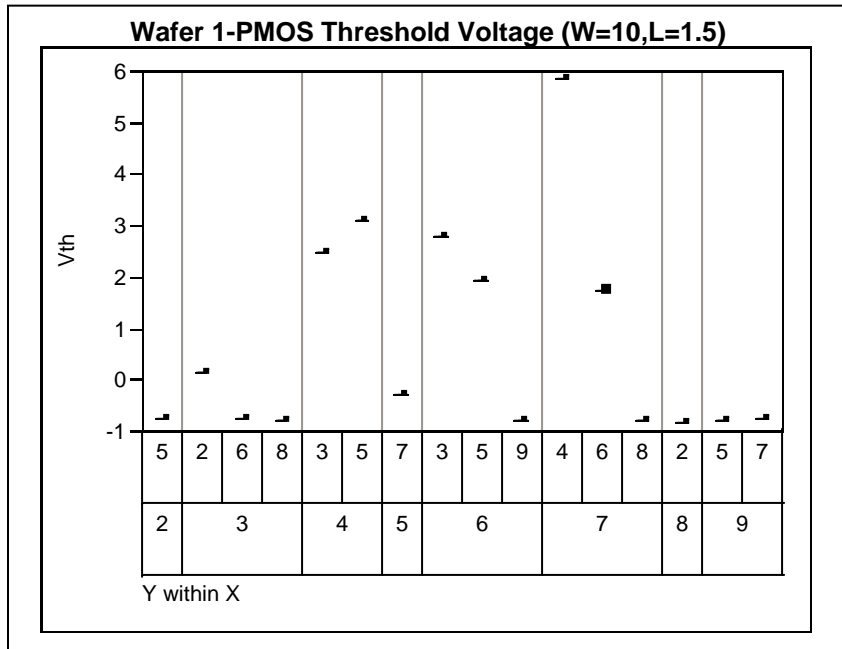
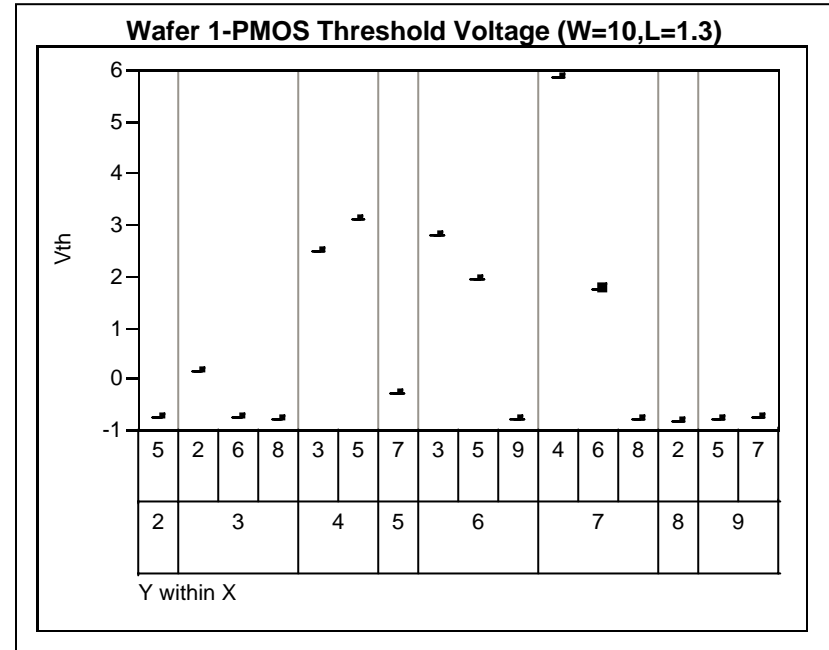
Wafer 1-NMOS Threshold Voltage (W=10,L=25)



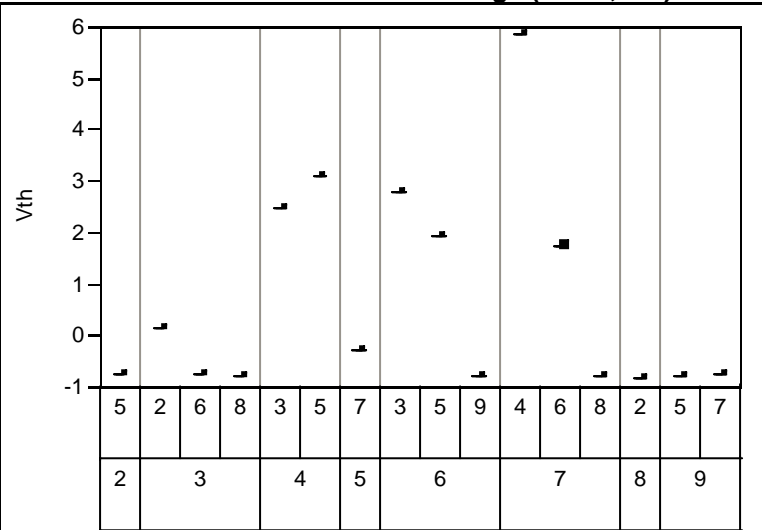
LCL=0V,UCL= -3V for PMOS.



W=10,L=Varied.

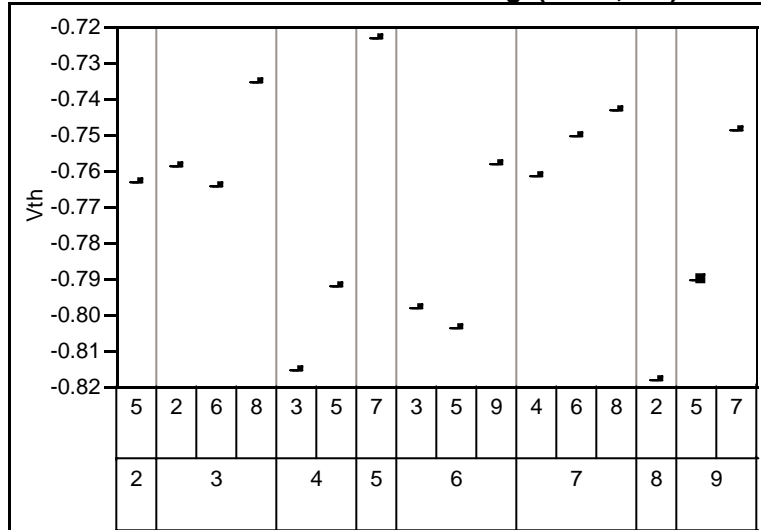


Wafer 1-PMOS Threshold Voltage (W=10,L=3)



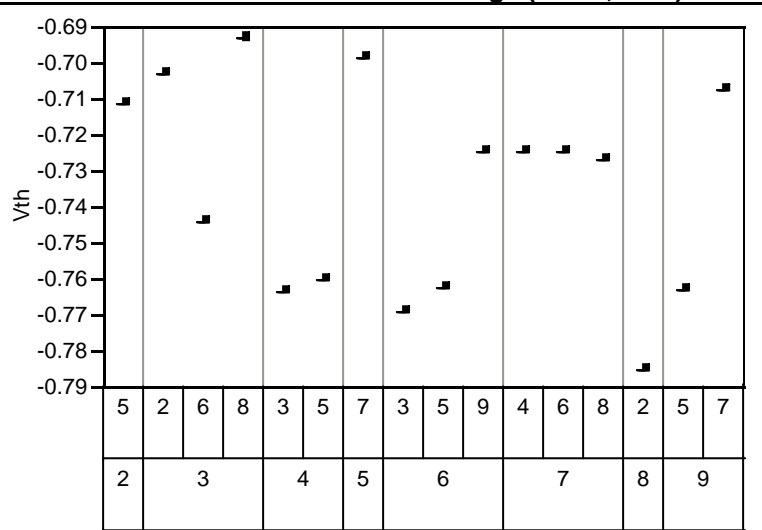
Y within X

Wafer 1-PMOS Threshold Voltage(W=10,L=5)



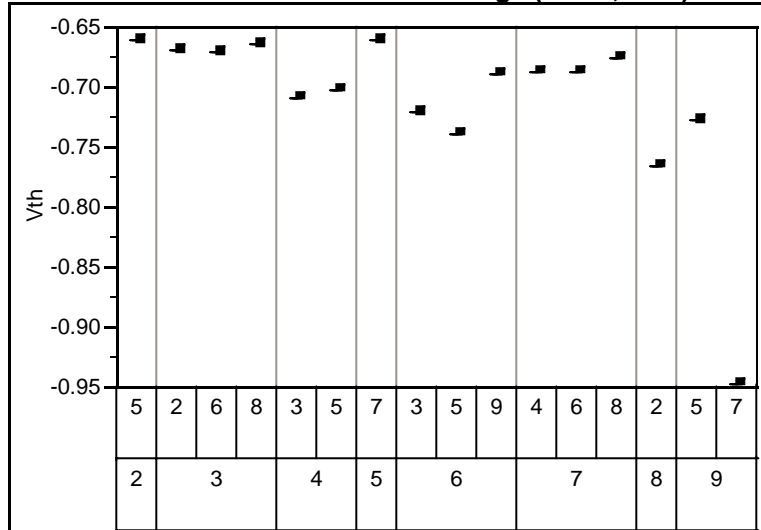
Y within X

Wafer 1- PMOS Threshold Voltage (W=10,L=10)



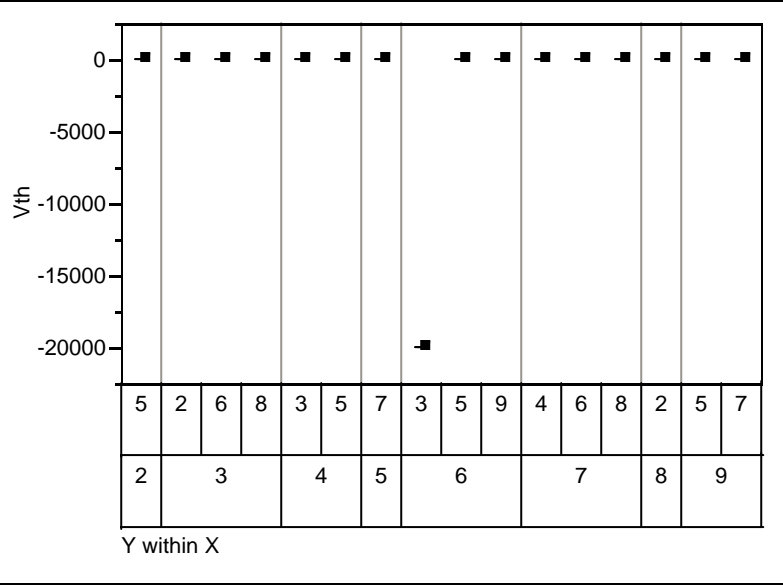
Y within X

Wafer 1-PMOS Threshold Voltage (W=10,L=25)

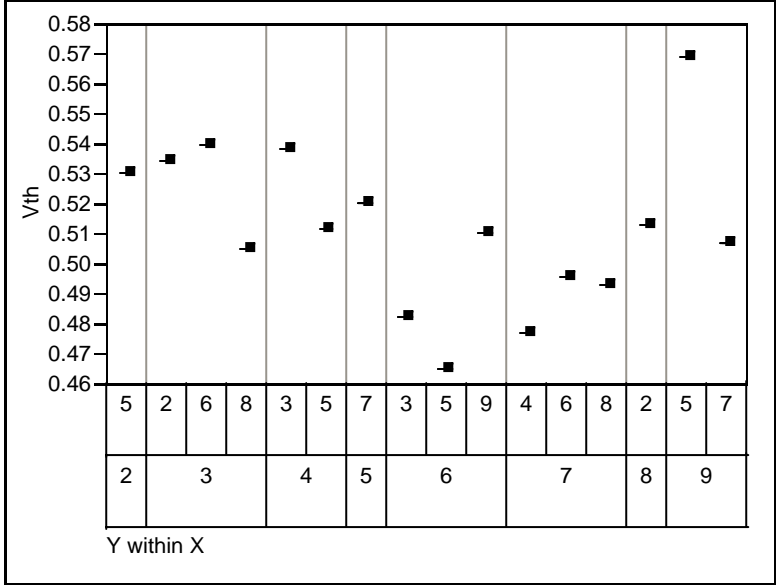


Y within X

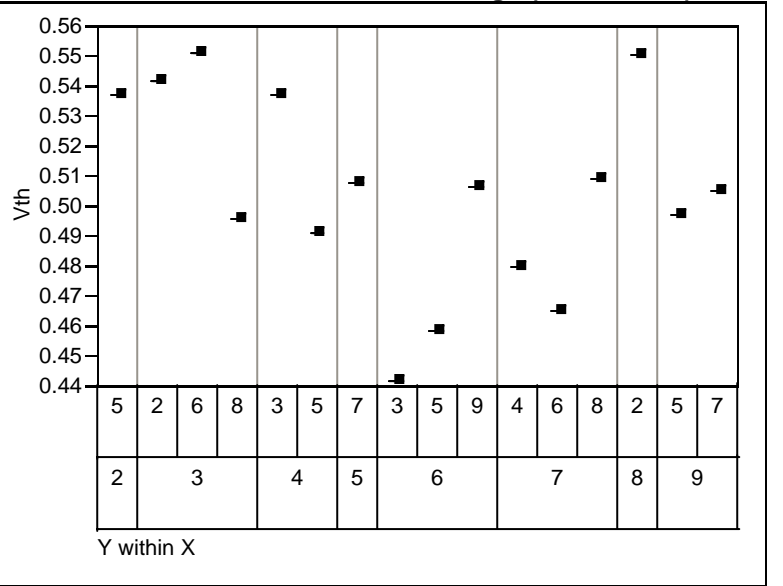
Wafer 2-NMOS Threshold Voltage (W=10,L=1)



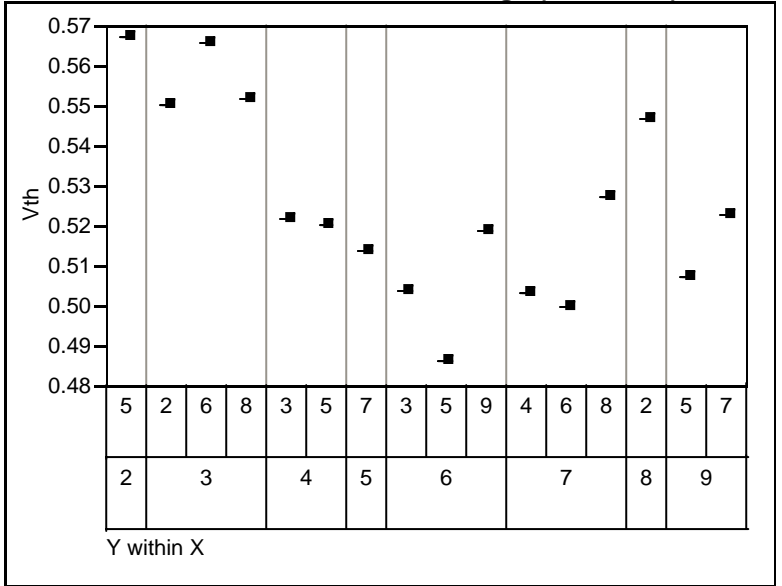
Wafer 2-NMOS Threshold Voltage (W=10,L=1.3)



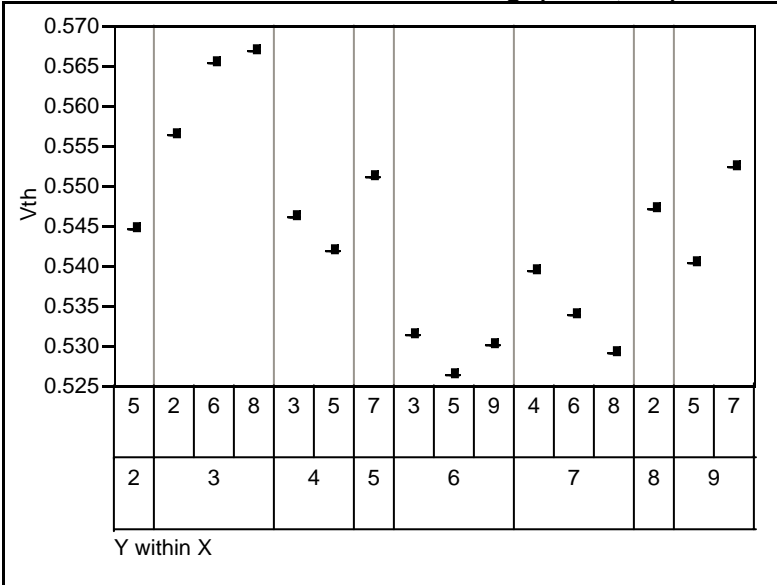
Wafer 2-NMOS Threshold Voltage (W=10,L=1.5)



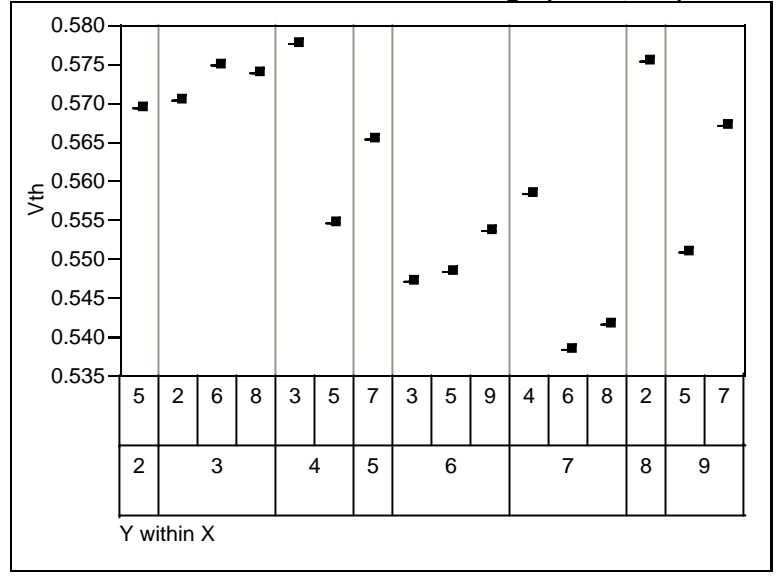
Wafer 2-NMOS Threshold Voltage (W=10,L=2)



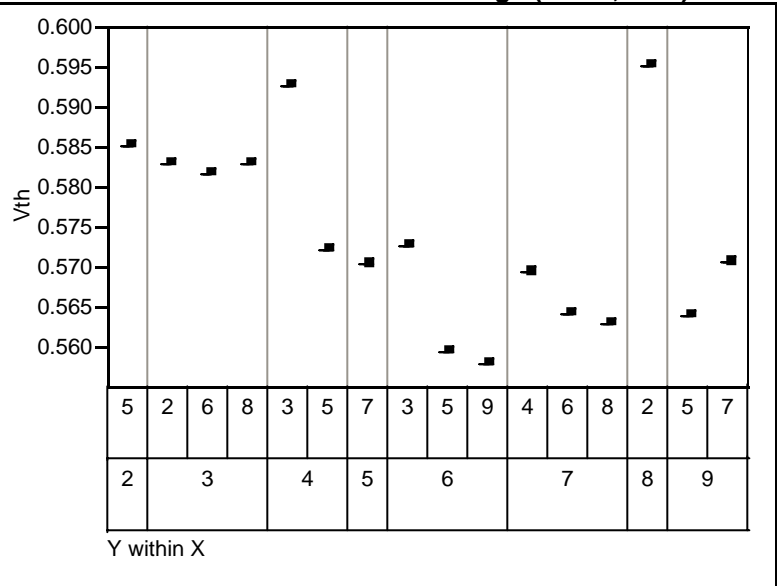
Wafer 2-NMOS Threshold Voltage(W=10,L=3)



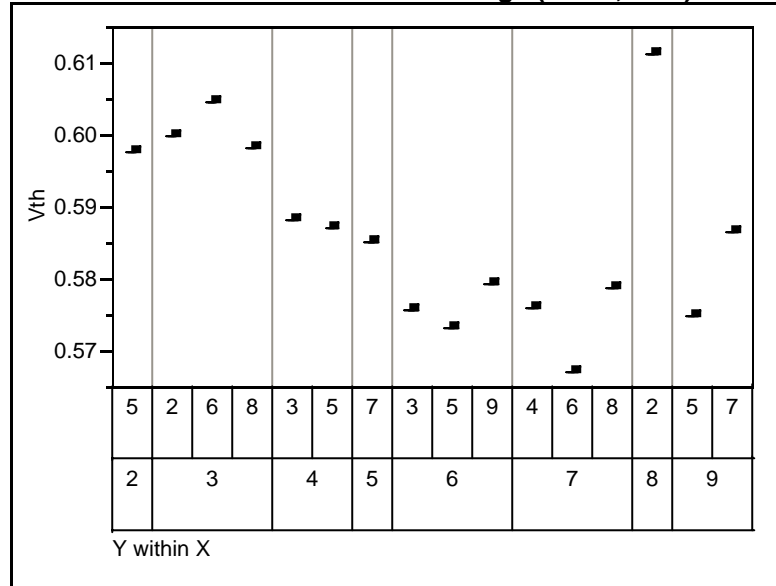
Wafer 2-NMOS Threshold Voltage (W=10,L=5)



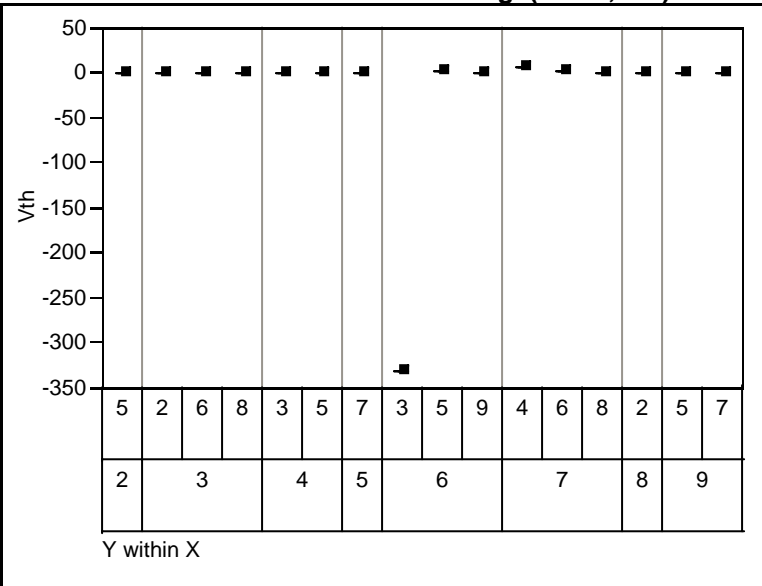
Wafer 2-NMOS Threshold Voltage (W=10,L=10)



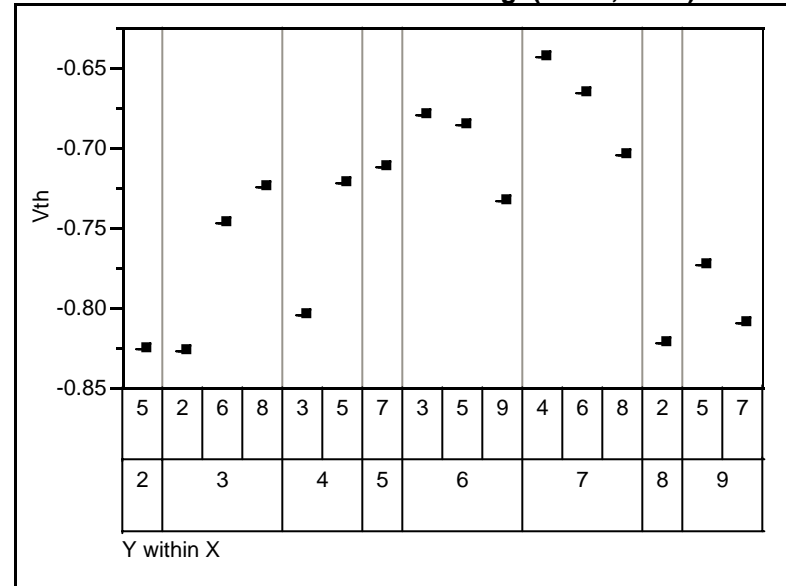
Wafer 2-NMOS Threshold Voltage (W=10,L=25)



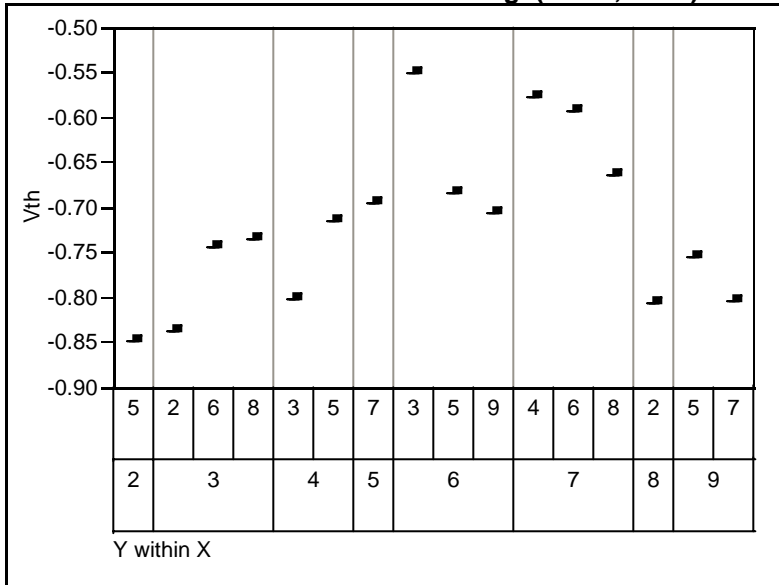
Wafer 2-PMOS Threshold Voltage(W=10,L=1)



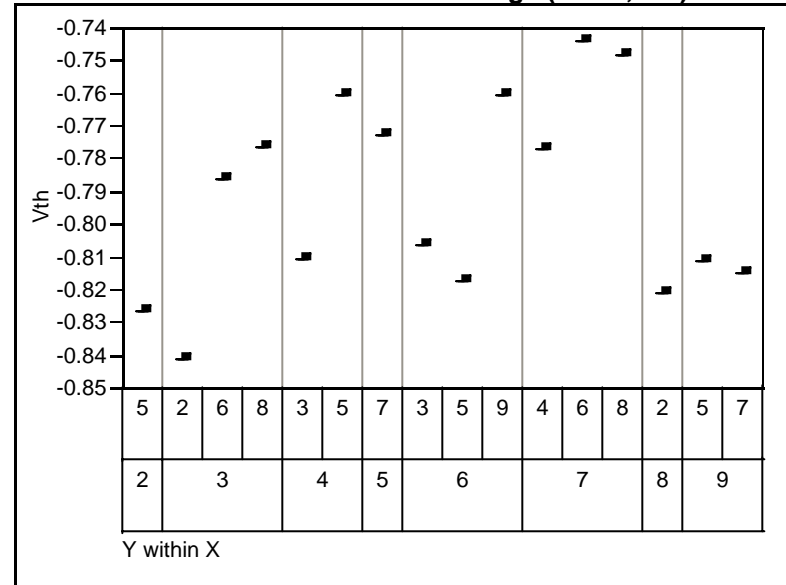
Wafer 2-PMOS Threshold Voltage(W=10,L=1.3)



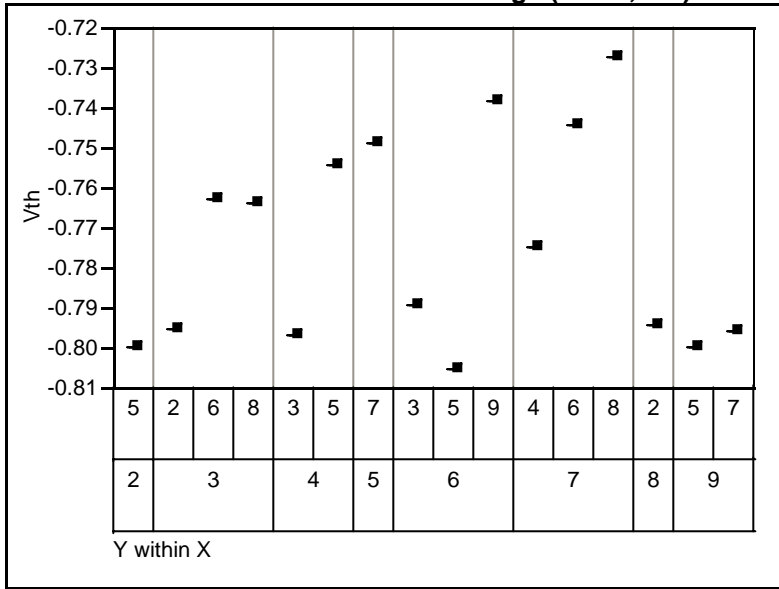
Wafer 2-PMOS Threshold Voltage(W=10,L=1.5)



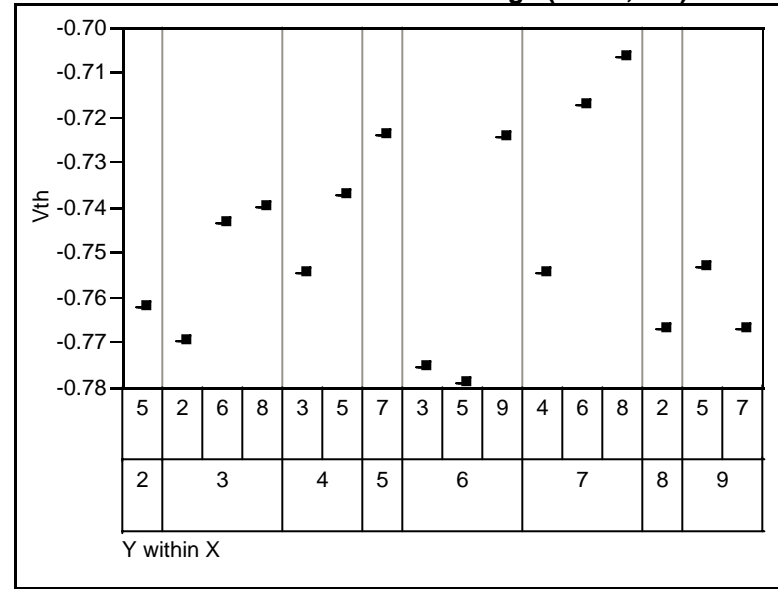
Wafer 2-PMOS Threshold Voltage (W=10,L=2)



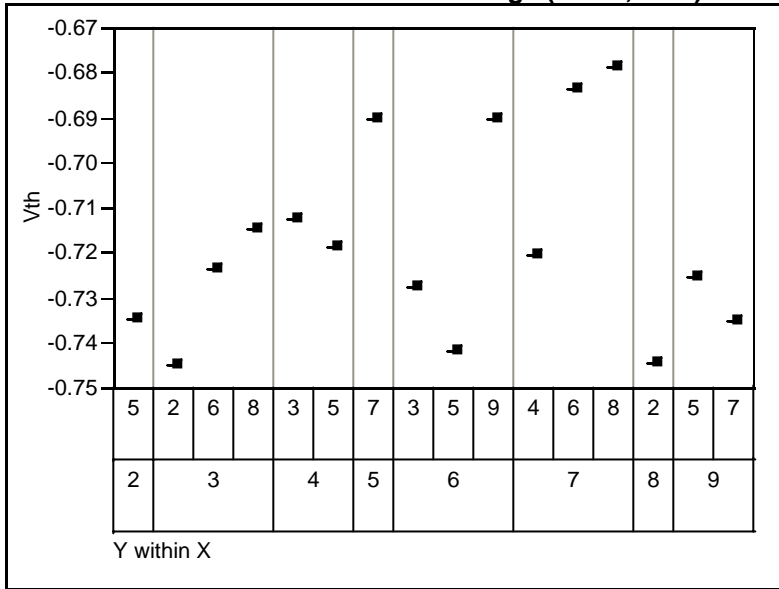
Wafer 2-PMOS Threshold Voltage (W=10,L=3)



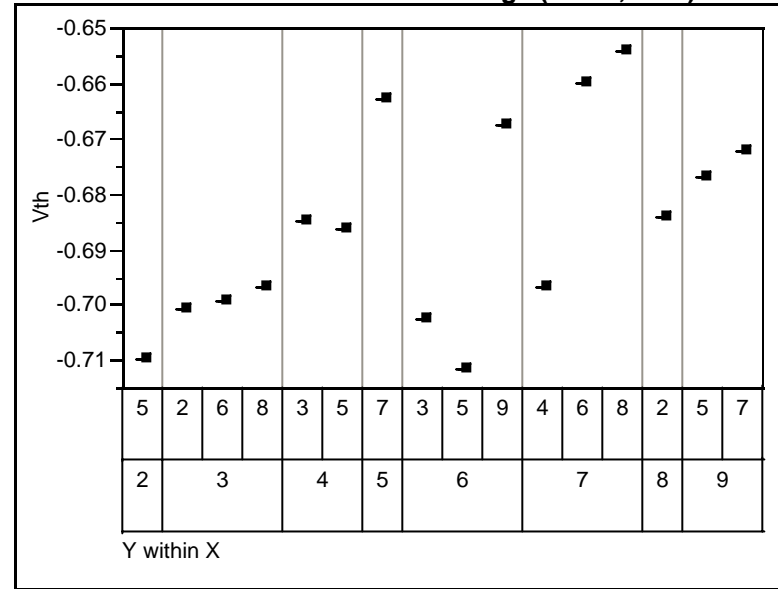
Wafer 2-PMOS Threshold Voltage (W=10,L=5)



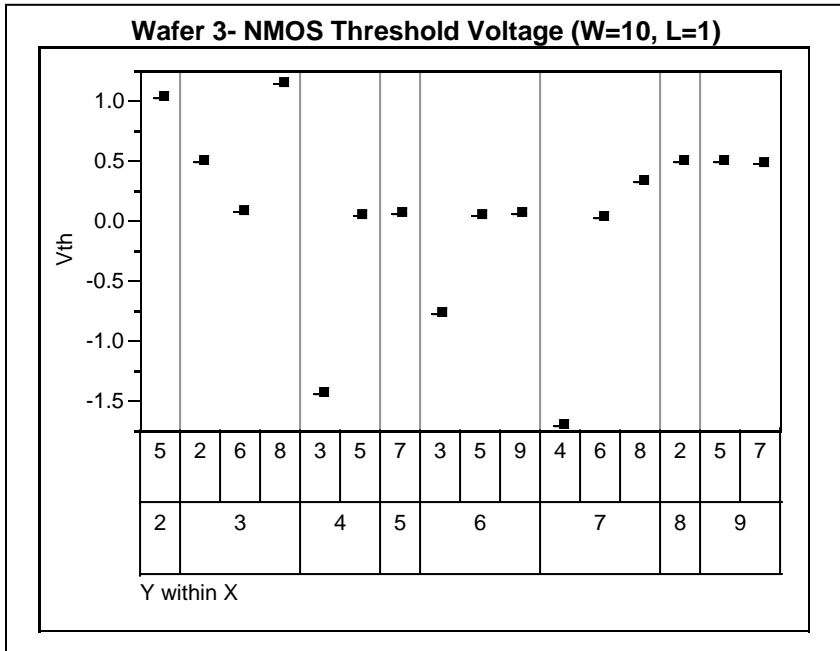
Wafer 2- PMOS Threshold Voltage (W=10,L=10)



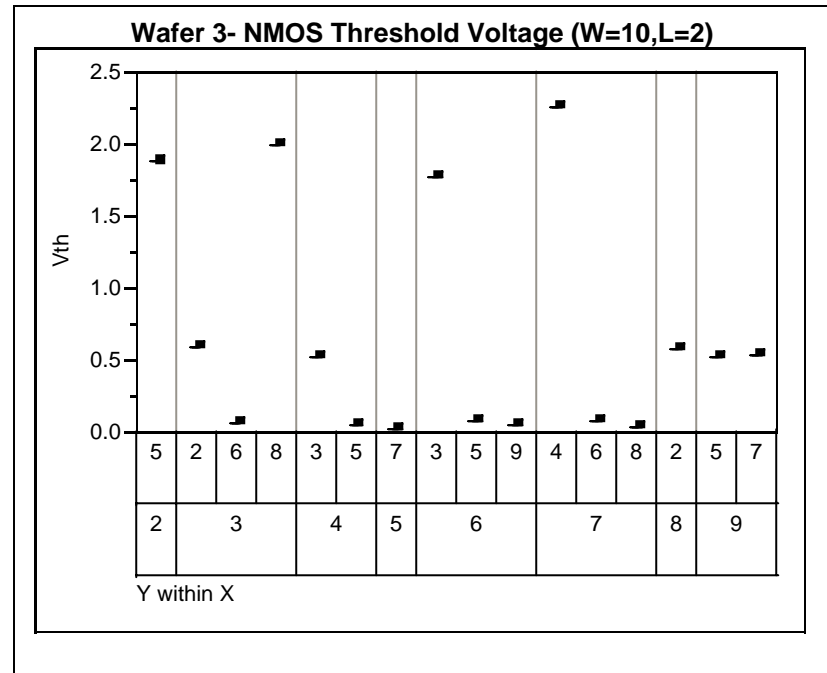
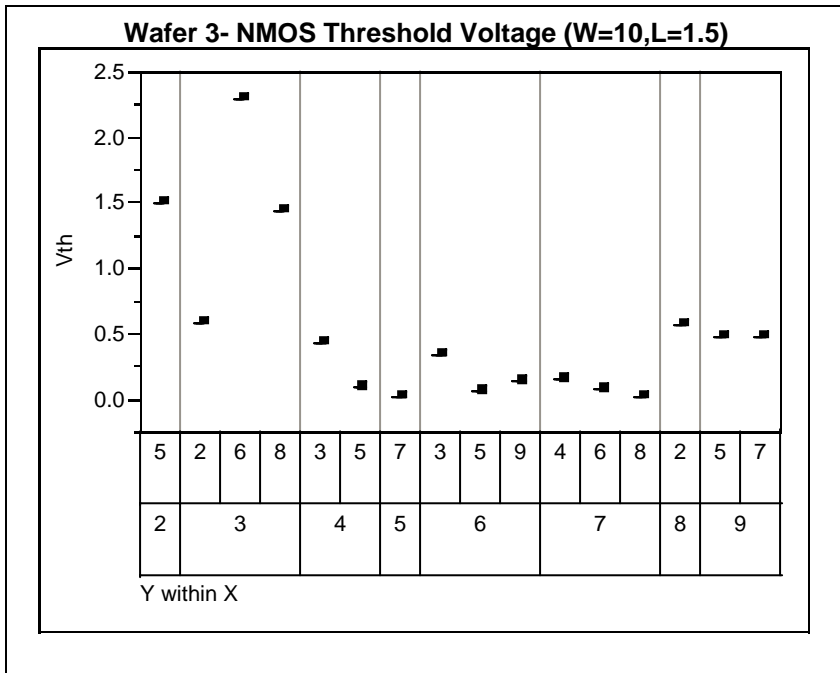
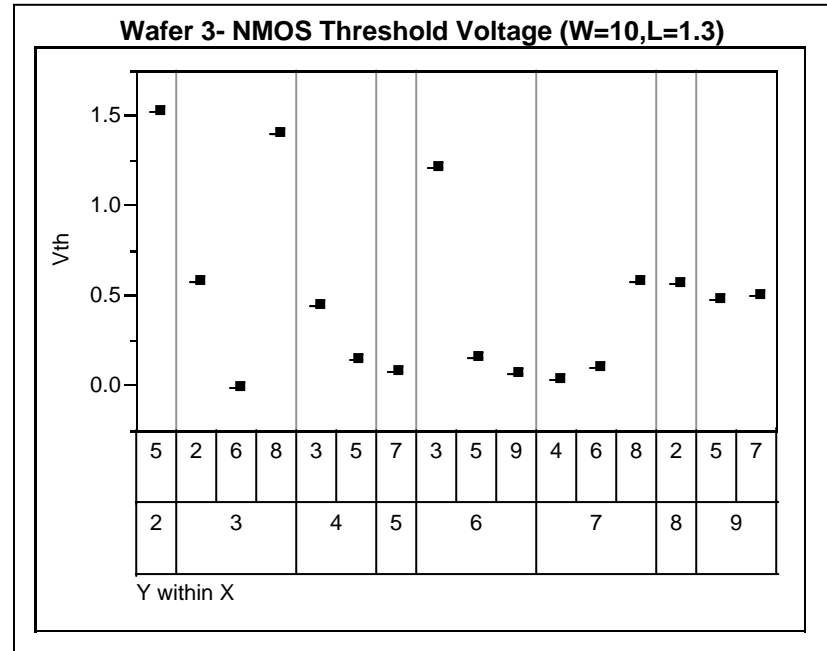
Wafer 2-PMOS Threshold Voltage (W=10,L=25)



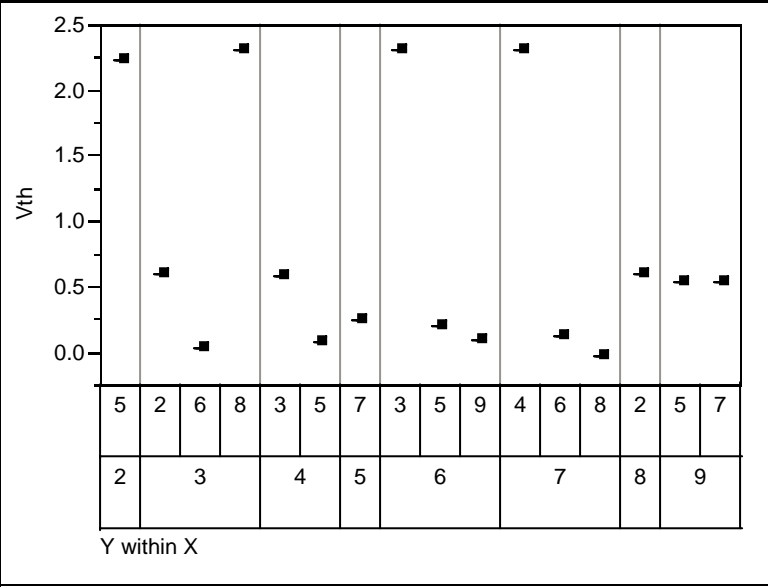
LCL=0,UCL=3V for NMOS.



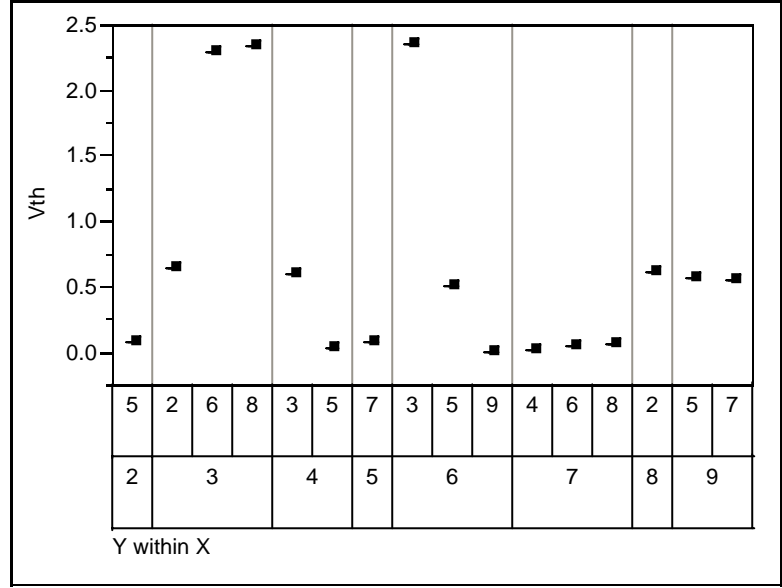
W=10, L=varied.



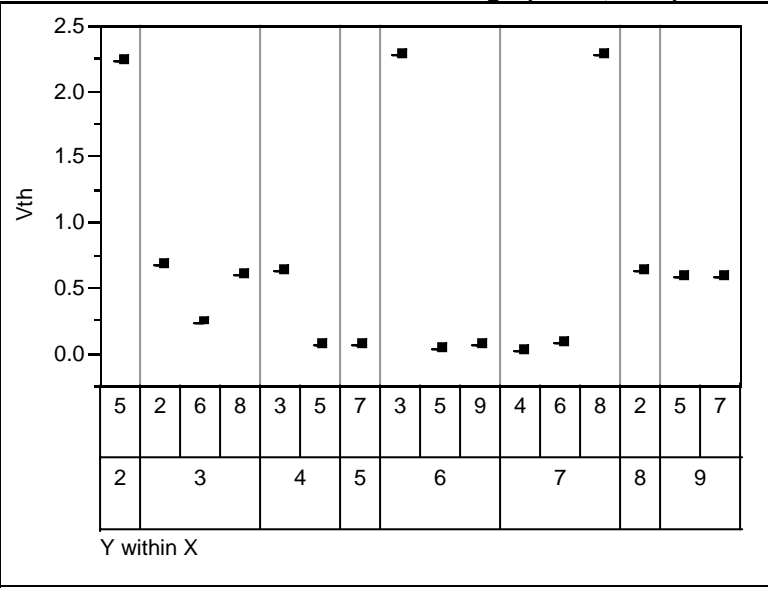
Wafer 3- NMOS Threshold Voltage (W=10,L=3)



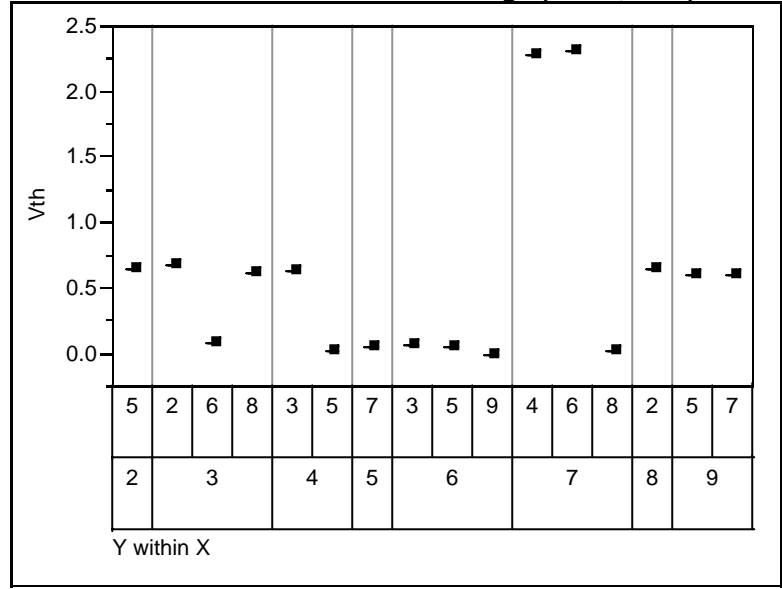
Wafer 3- NMOS Threshold Voltage (W=10,L=5)



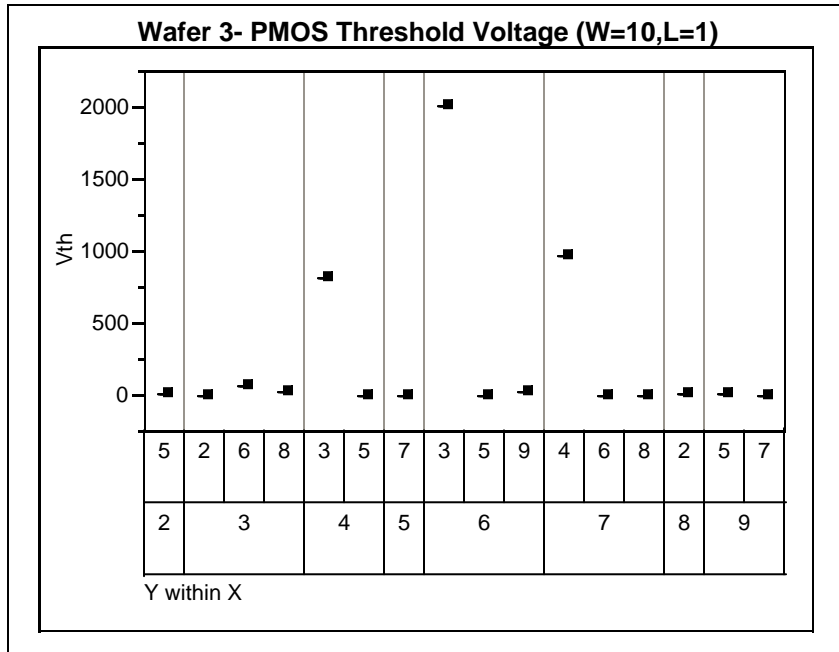
Wafer 3-NMOS Threshold Voltage (W=10,L=10)



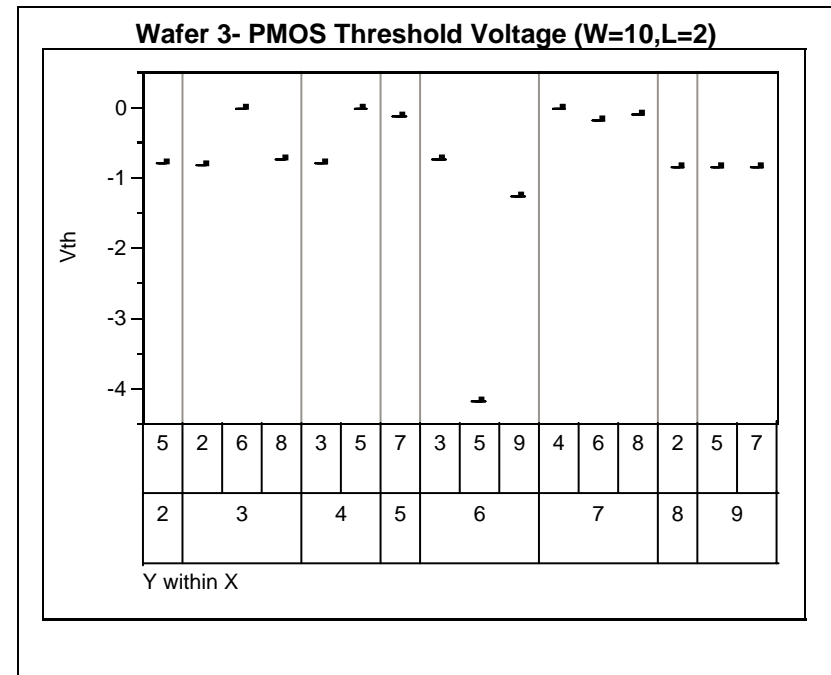
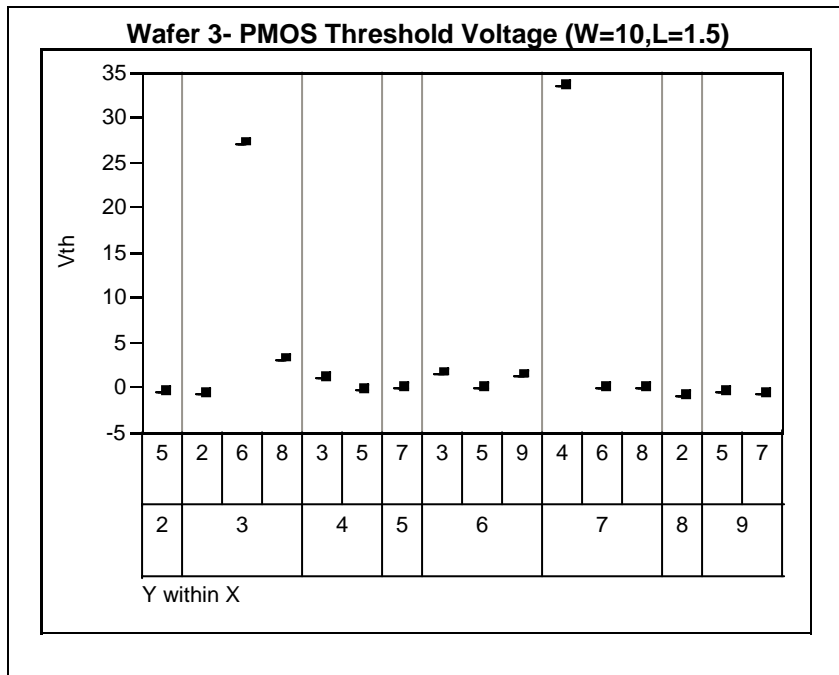
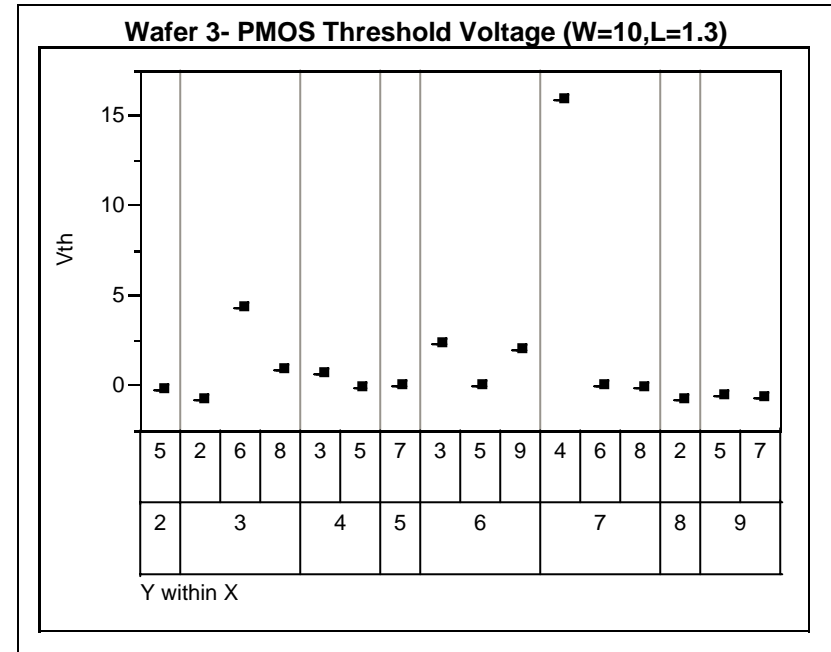
Wafer 3- NMOS Threshold Voltage (W=10,L=25)



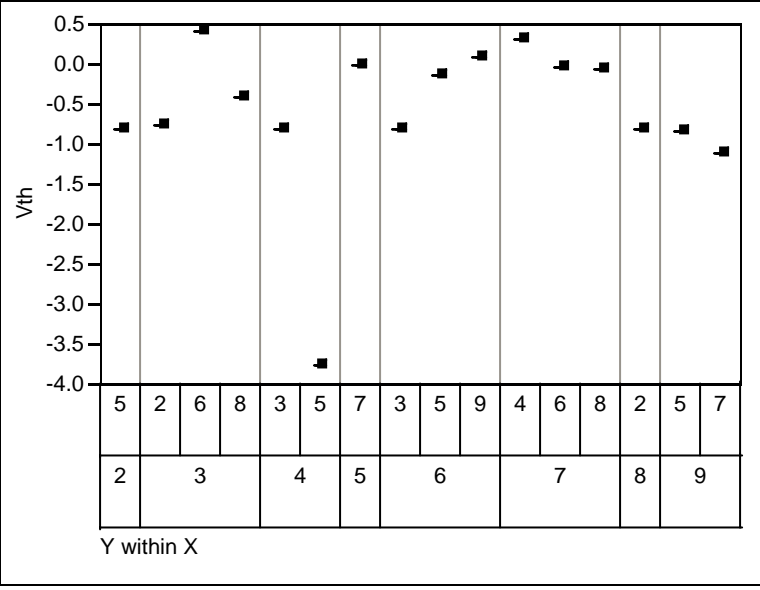
LCL= -3,UCL=0V for PMOS.



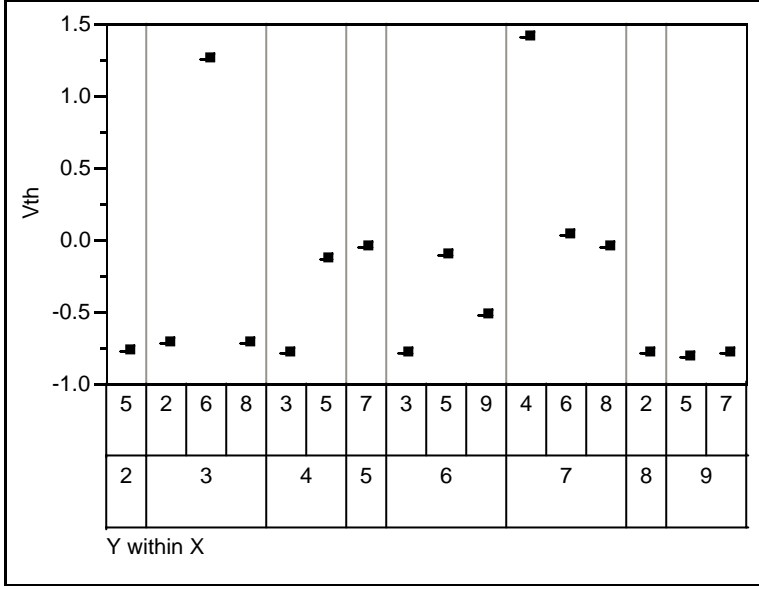
W=10,L=Varied.



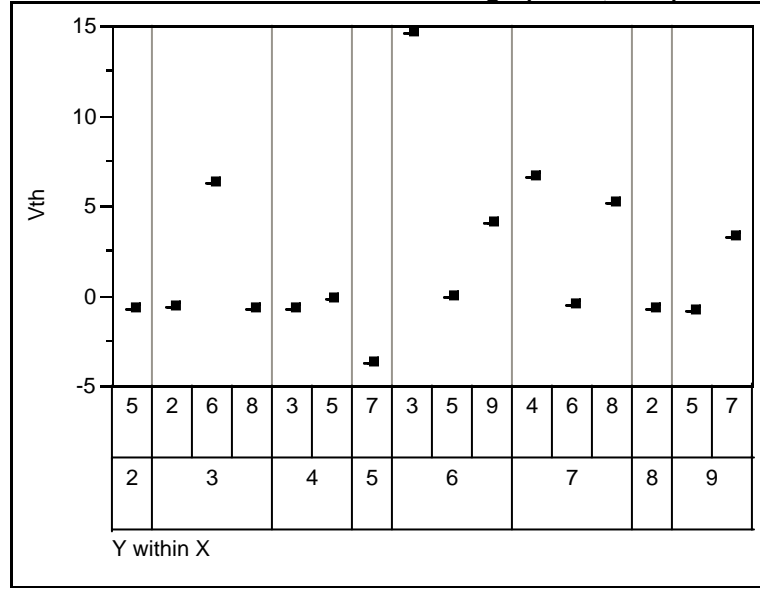
Wafer 3-PMOS Threshold Voltage (W=10,L=3)



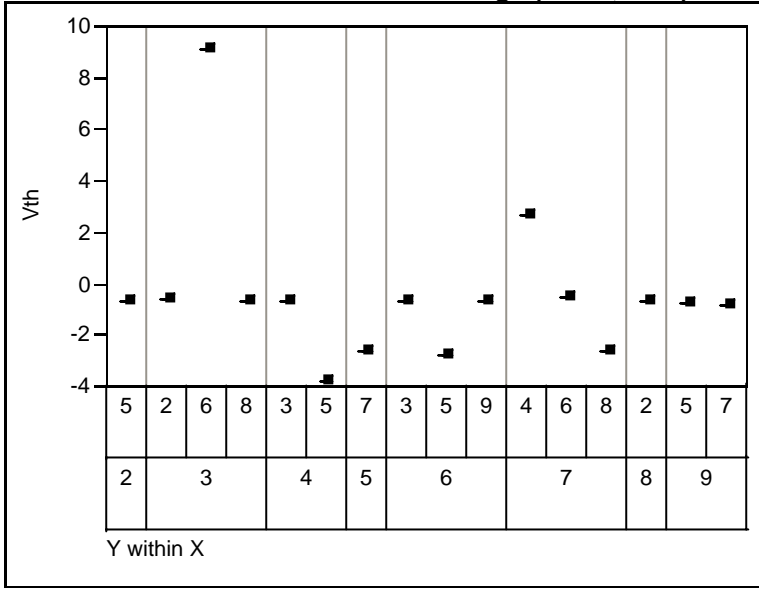
Wafer 3- PMOS Threshold Voltage (W=10,L=5)



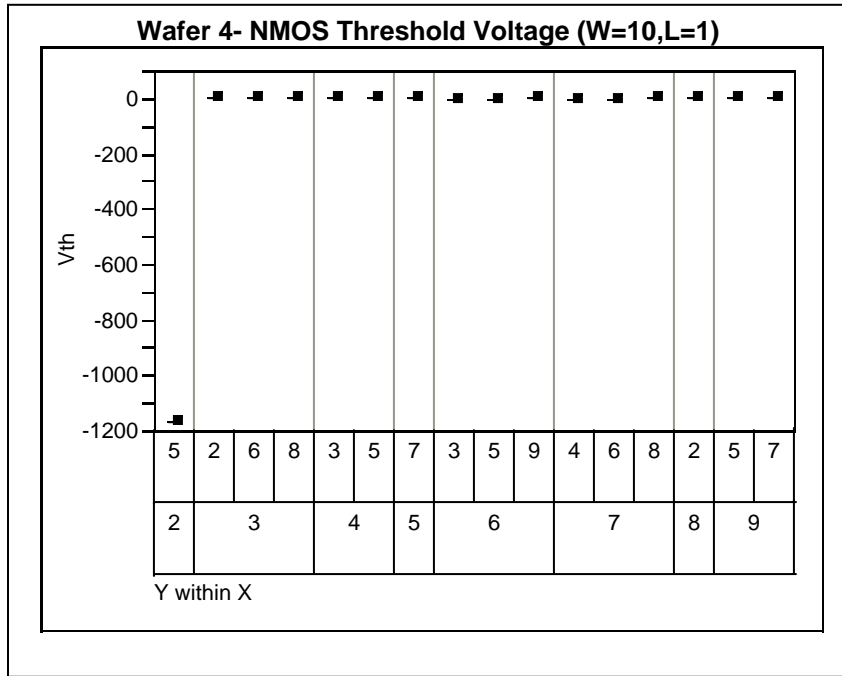
Wafer 3-PMOS Threshold Voltage (W=10,L=10)



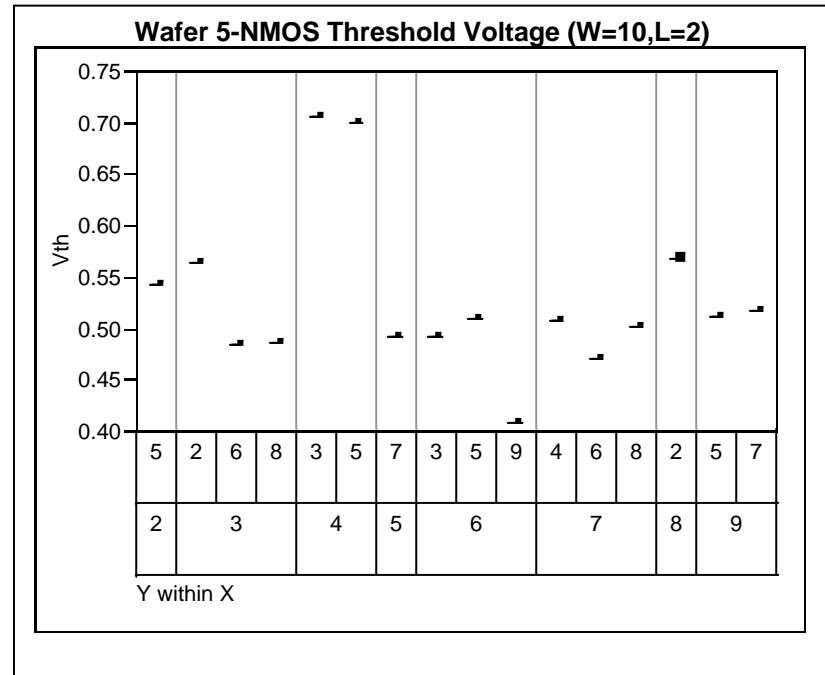
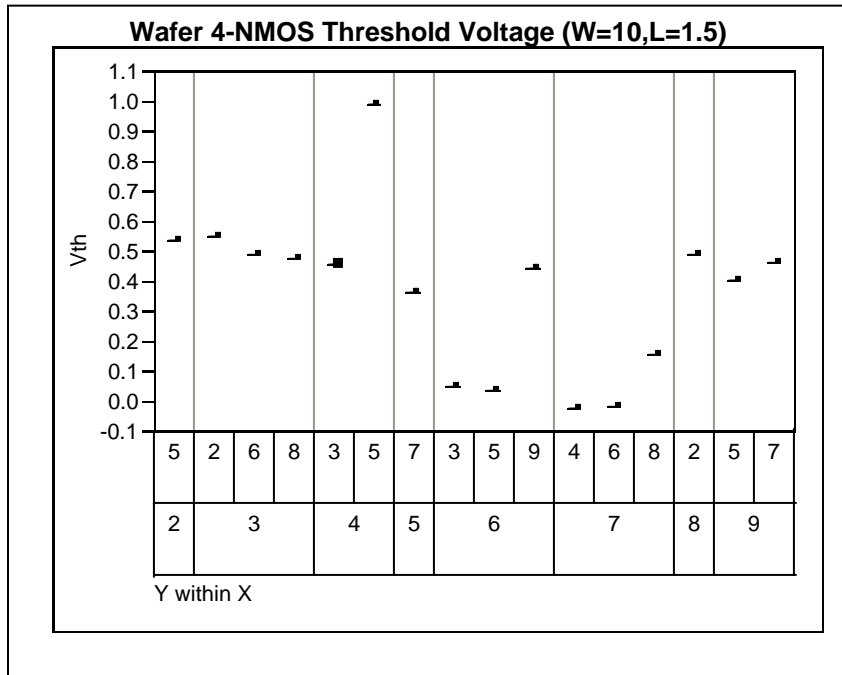
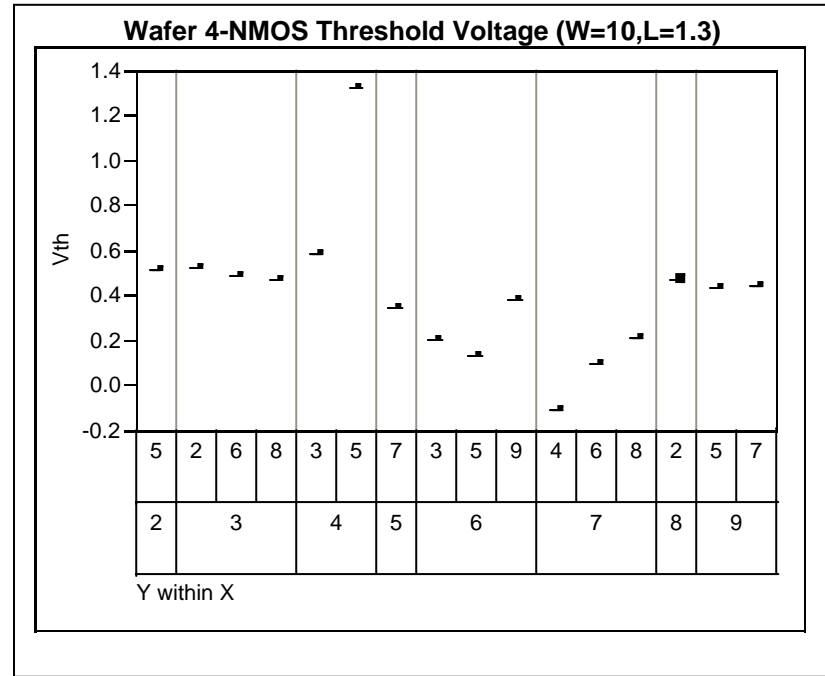
Wafer 3- PMOS Threshold Voltage (W=10,L=25)



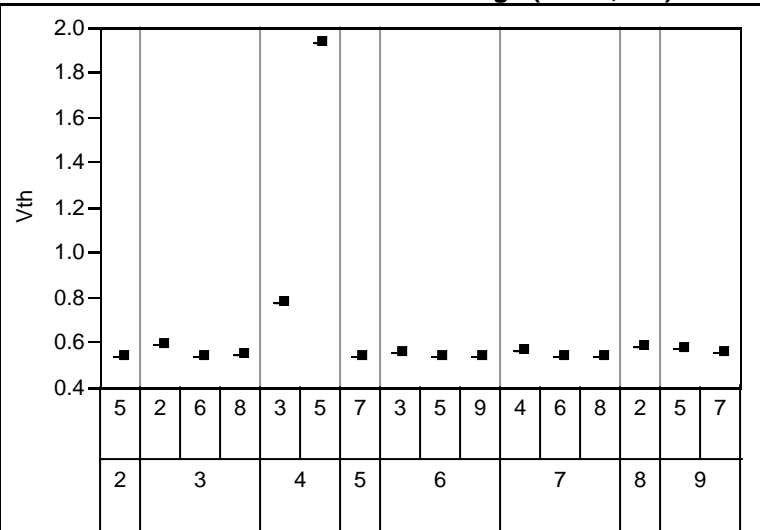
LCL=0,UCL=3V for NMOS.



W=10, L=varied.

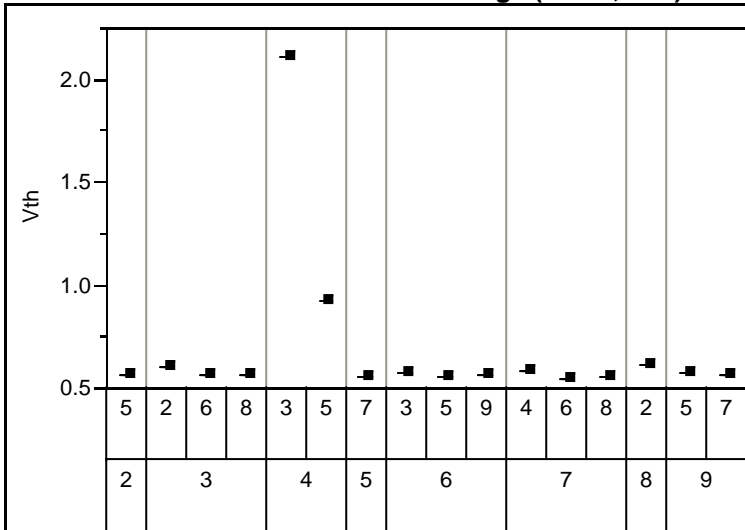


Wafer 4-NMOS Threshold Voltage (W=10,L=3)



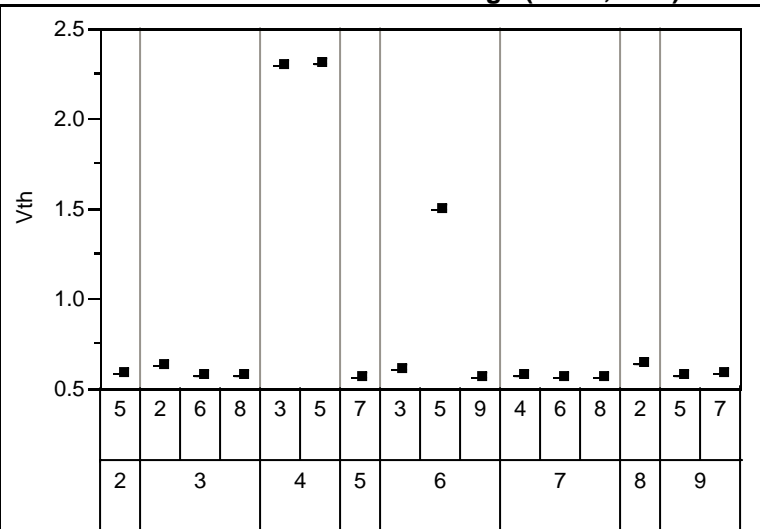
Y within X

Wafer 4-NMOS Threshold Voltage (W=10, L=5)



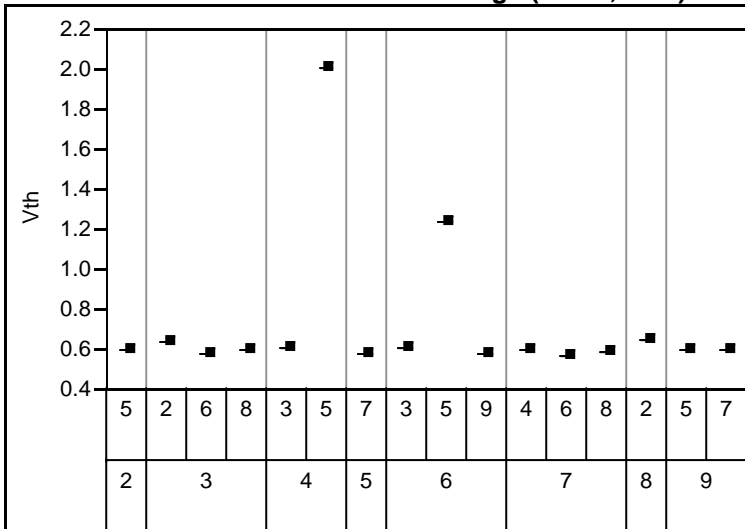
Y within X

Wafer 4-NMOS Threshold Voltage (W=10,L=10)



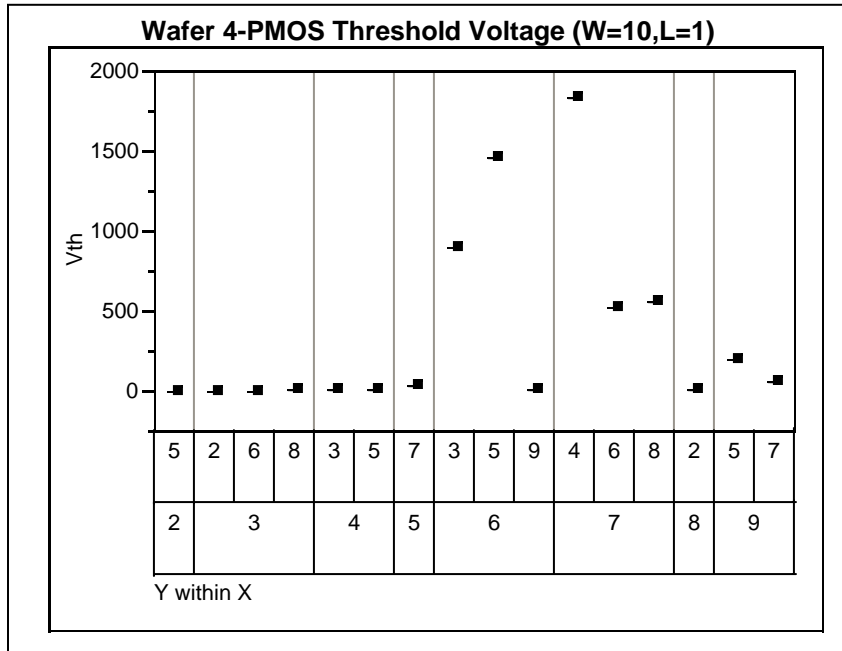
Y within X

Wafer 4-NMOS Threshold Voltage (W=10,L=25)

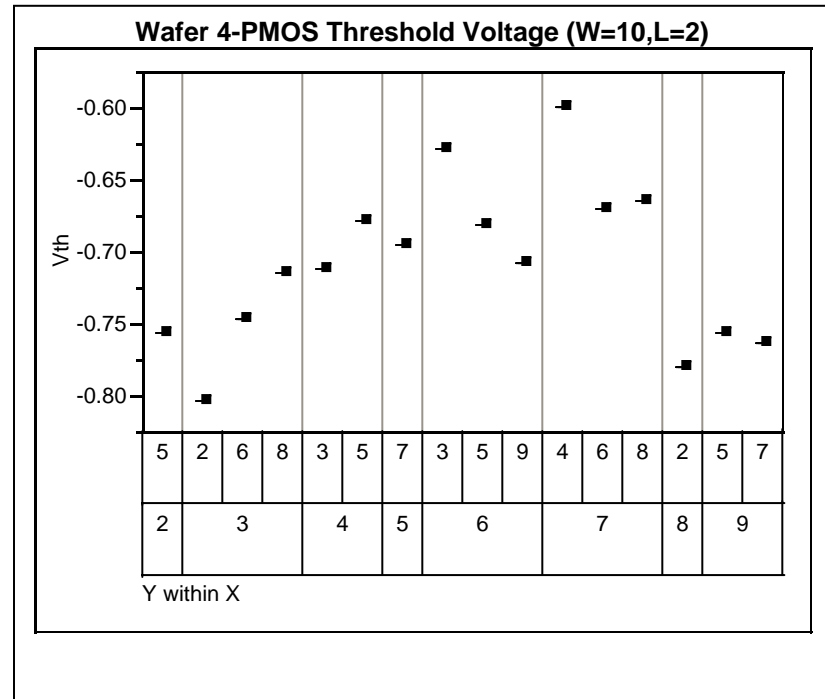
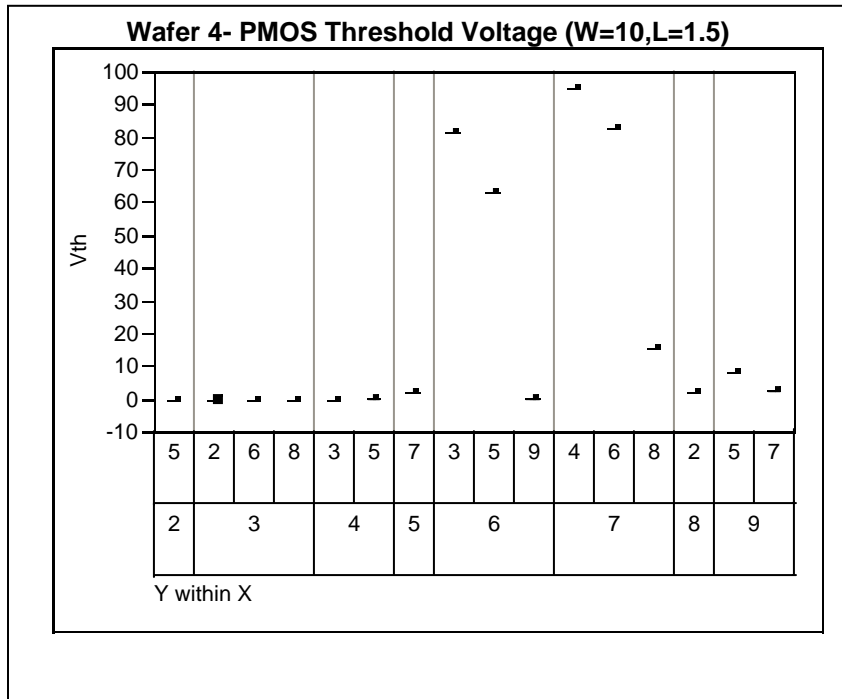
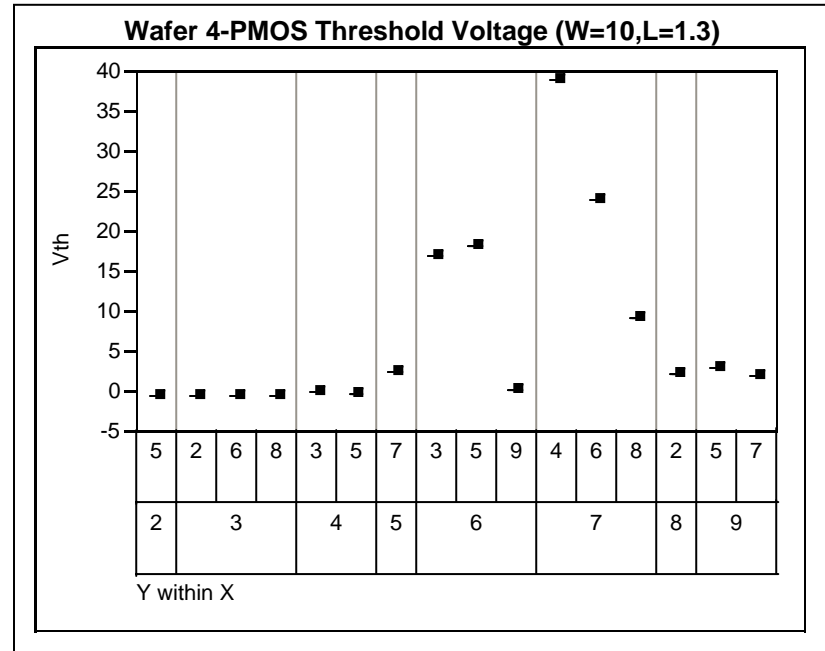


Y within X

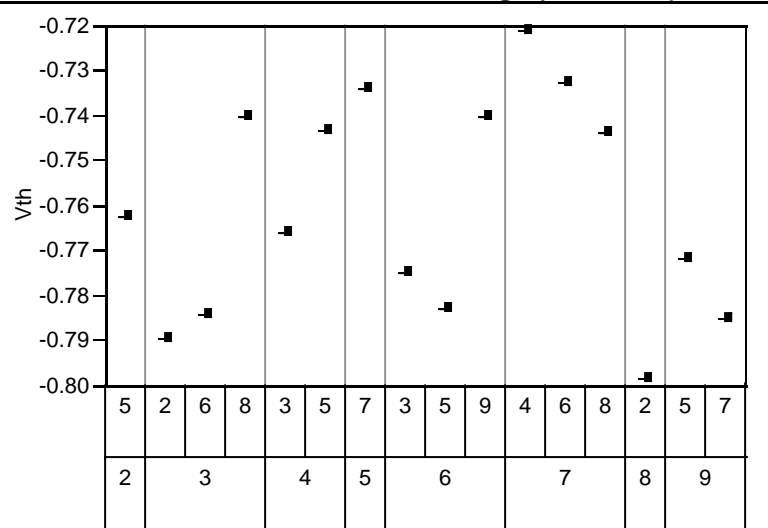
LCL= -3,UCL=0V for PMOS.



W=10, L=varied.

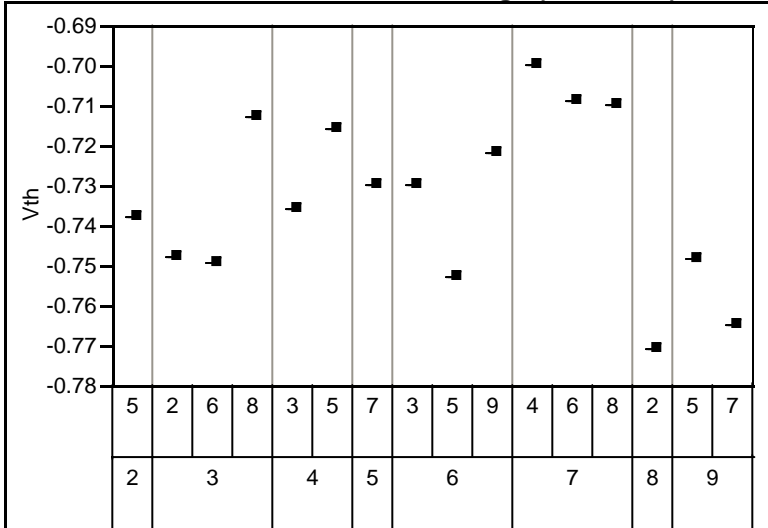


Wafer 4- PMOS Threshold Voltage (W=10,L=3)



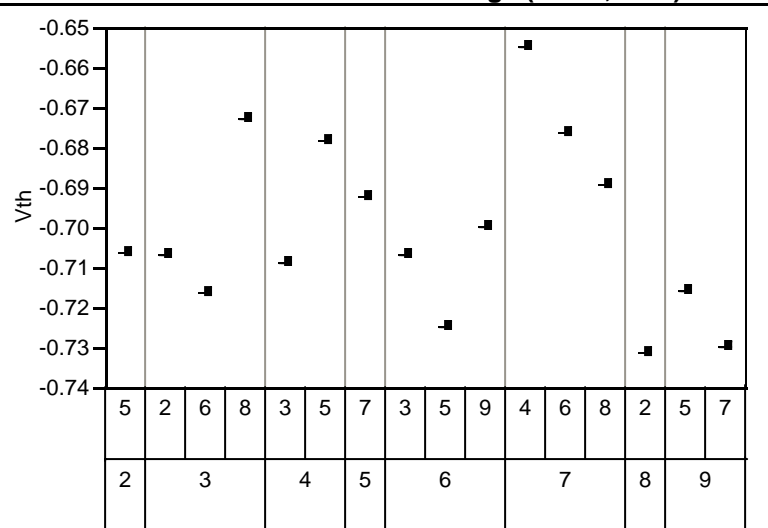
Y within X

Wafer 4-PMOS Threshold Voltage (W=10,L=5)



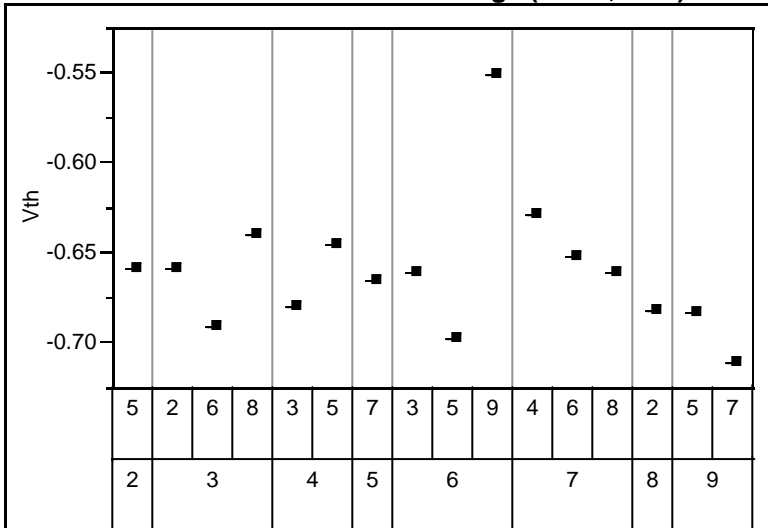
Y within X

Wafer 4-PMOS Threshold Voltage (W=10,L=10)



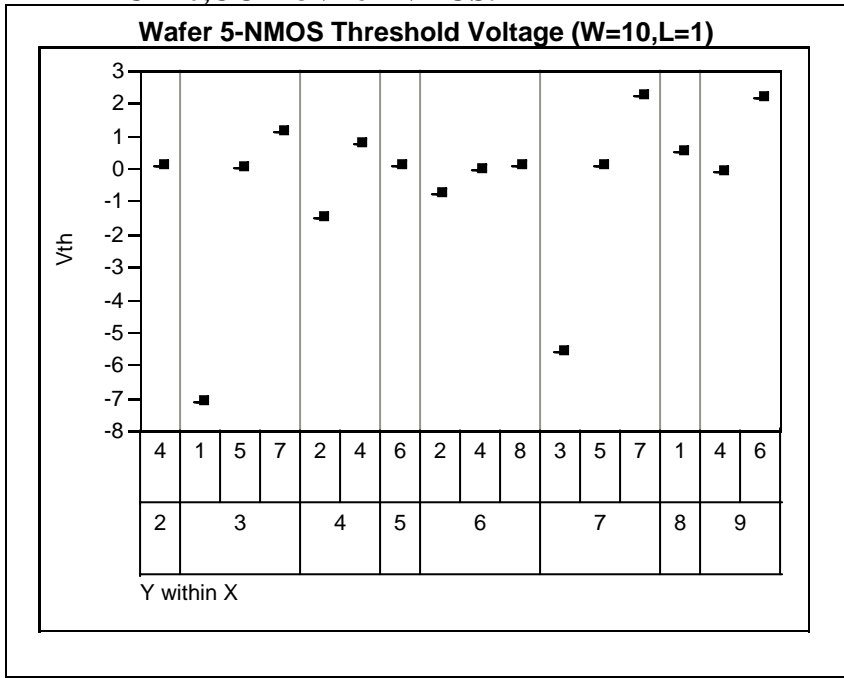
Y within X

Wafer 4-PMOS Threshold Voltage (W=10,L=25)

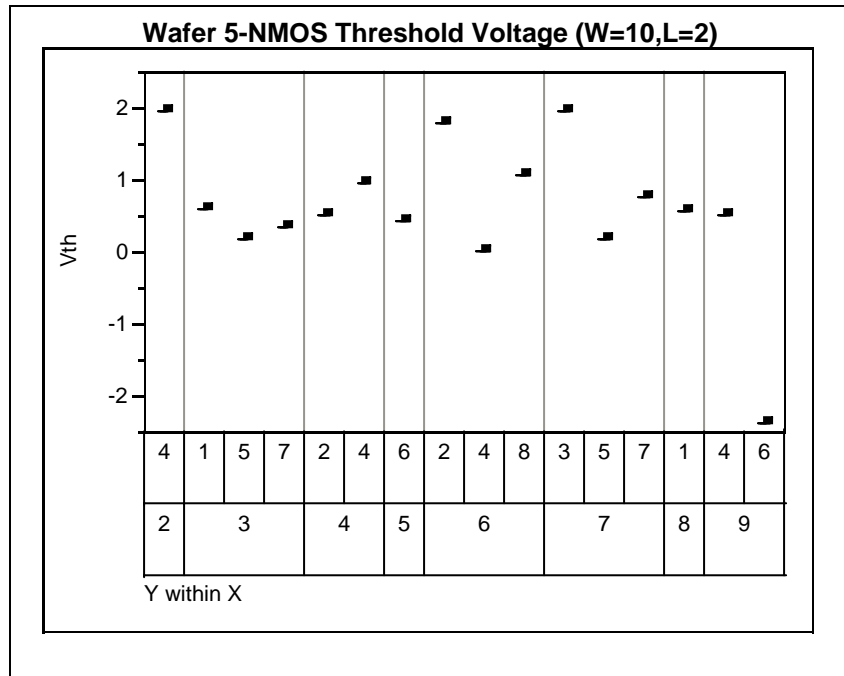
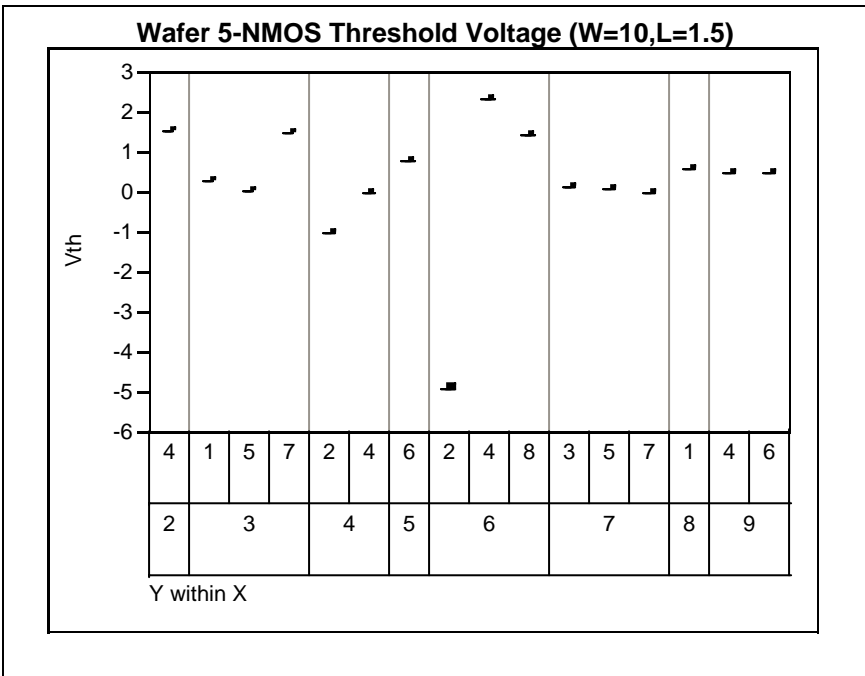
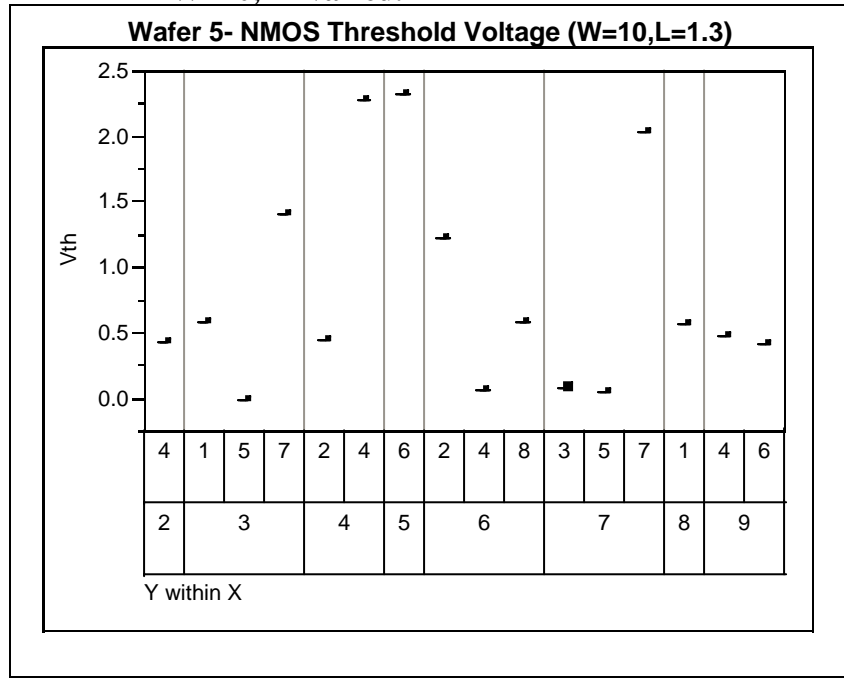


Y within X

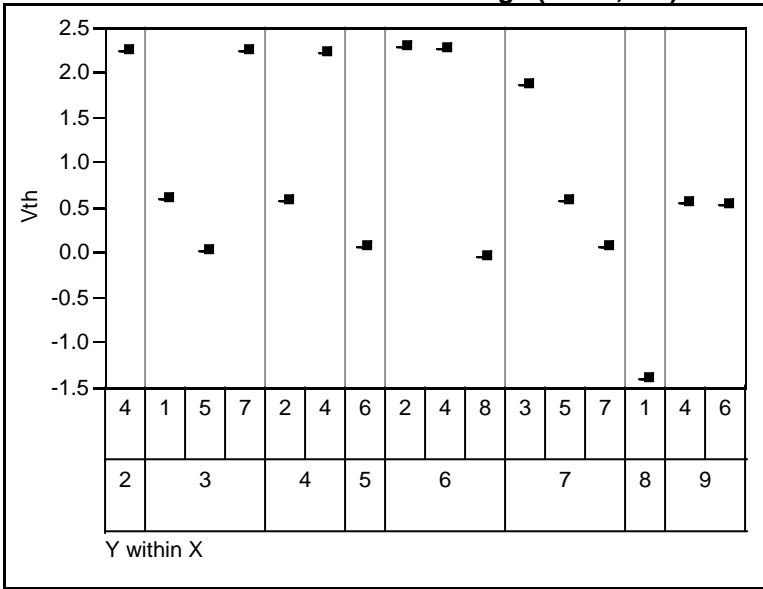
LCL=0,UCL=3V for NMOS.



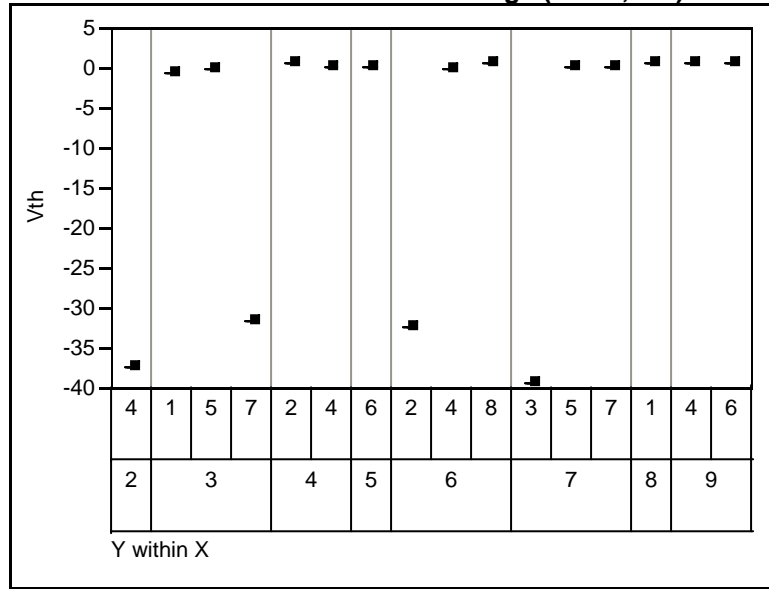
W=10, L=varied.



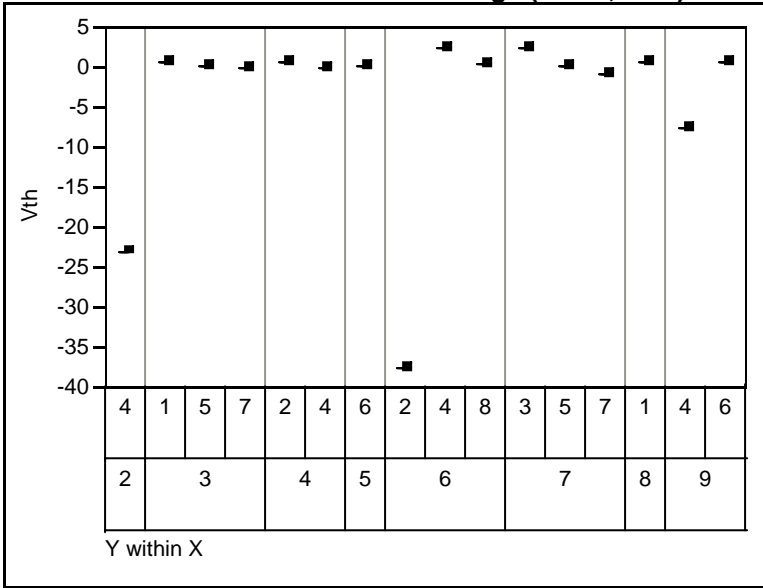
Wafer 5-NMOS Threshold Voltage (W=10,L=3)



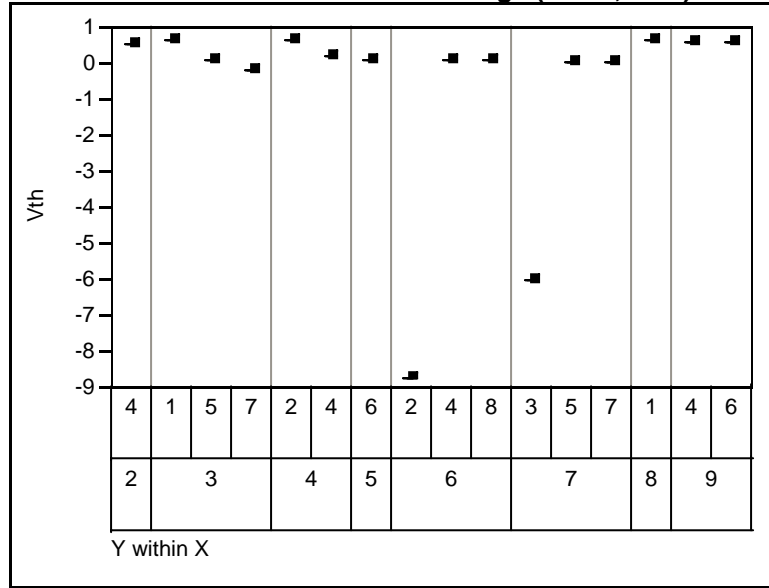
Wafer 5-NMOS Threshold Voltage (W=10,L=5)

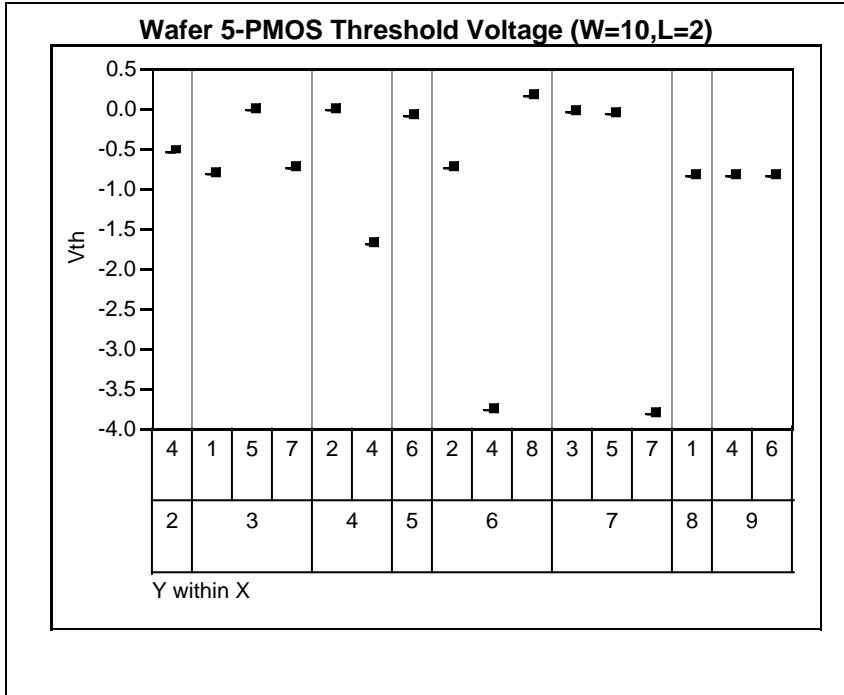
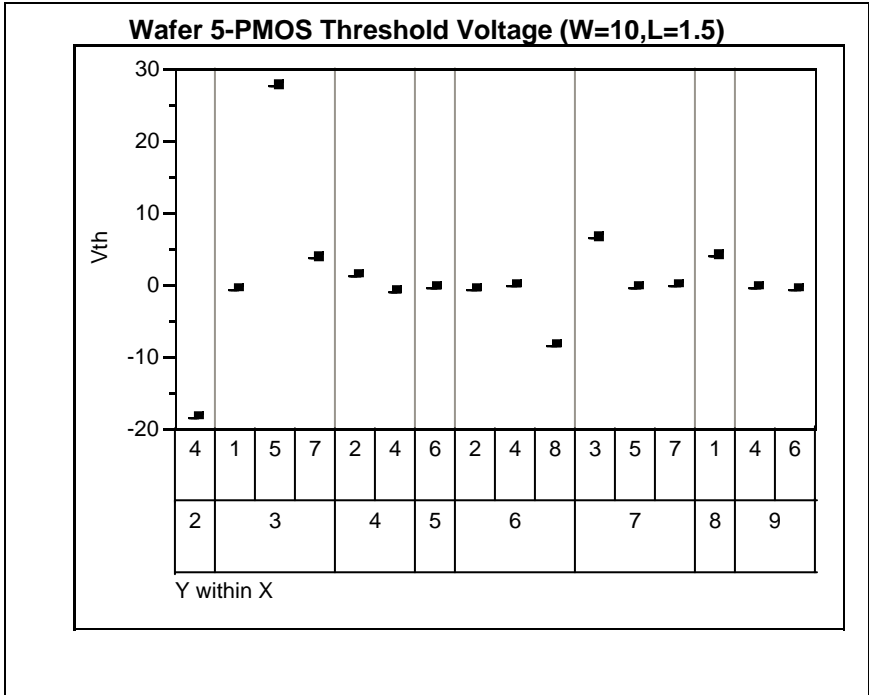
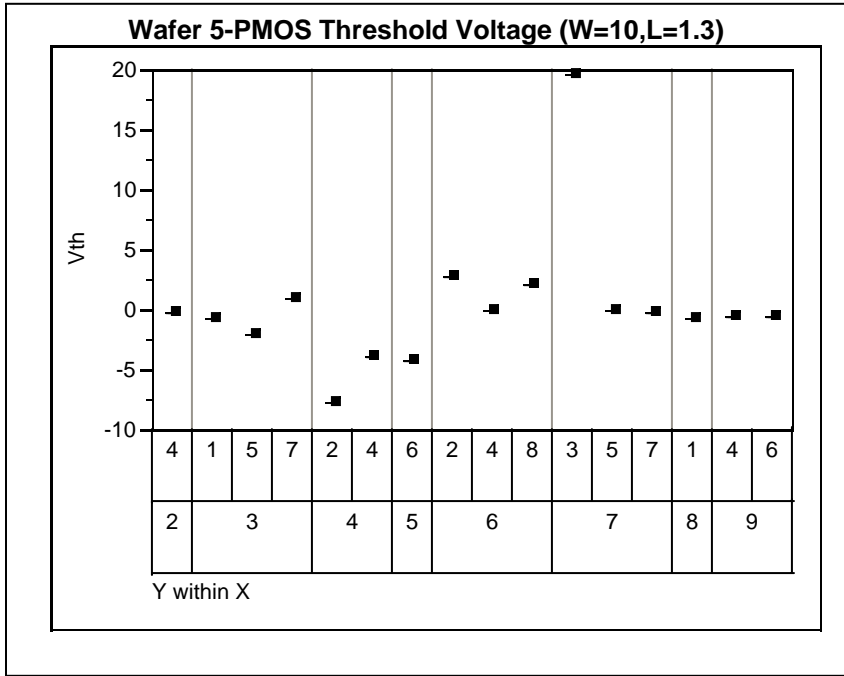
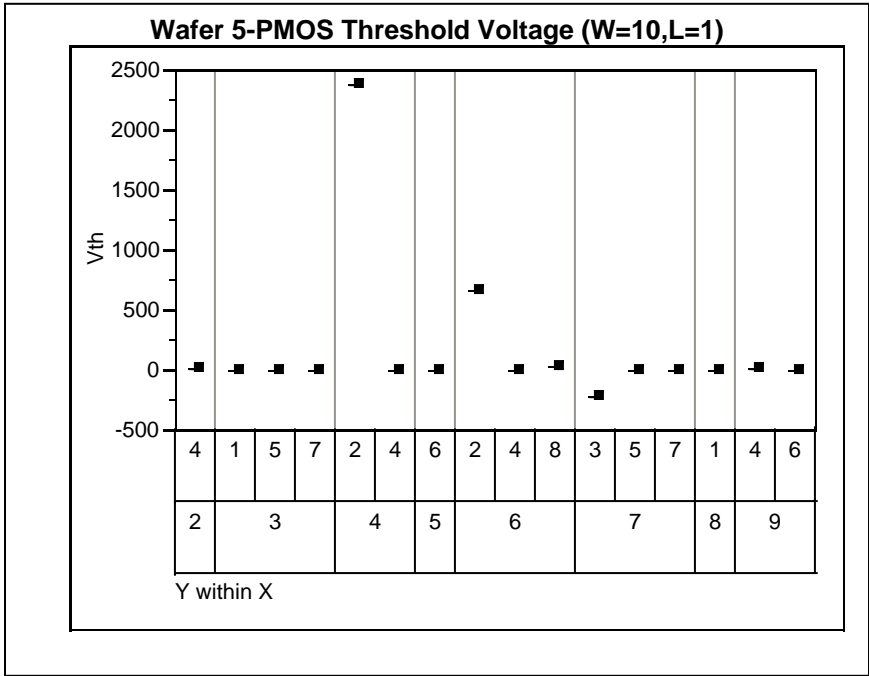


Wafer 5-NMOS Threshold Voltage (W=10,L=10)

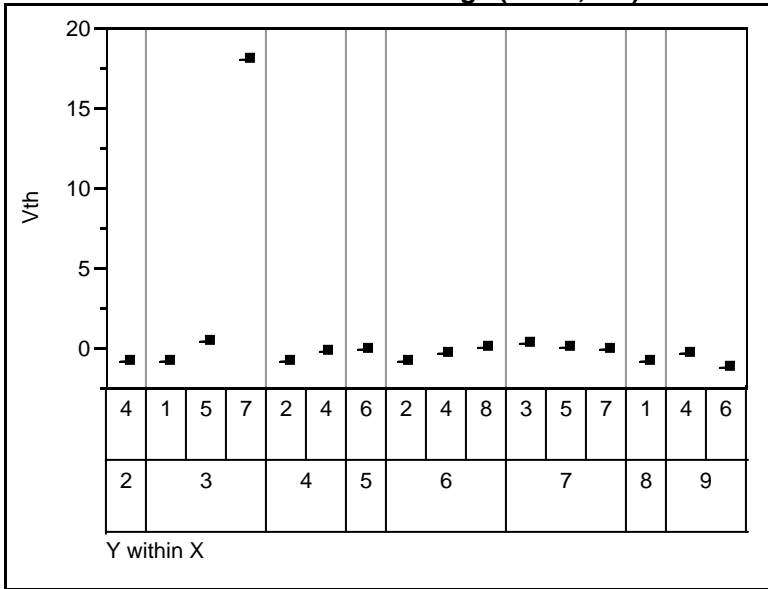


Wafer 5-NMOS Threshold Voltage (W=10,L=25)

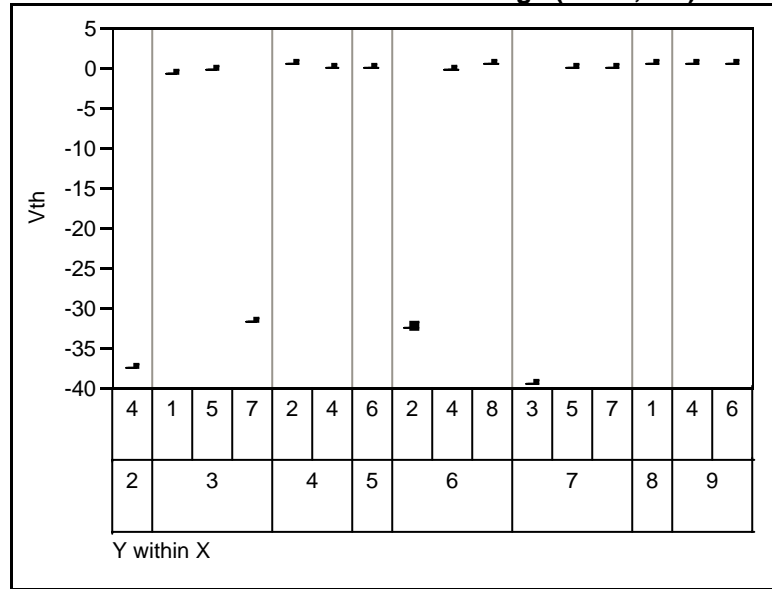




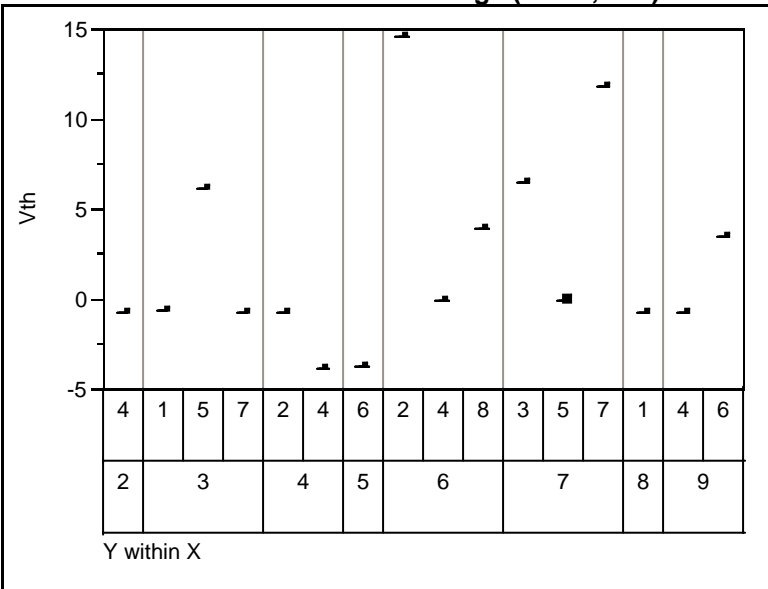
Wafer 5-PMOS Threshold Voltage (W=10,L=3)



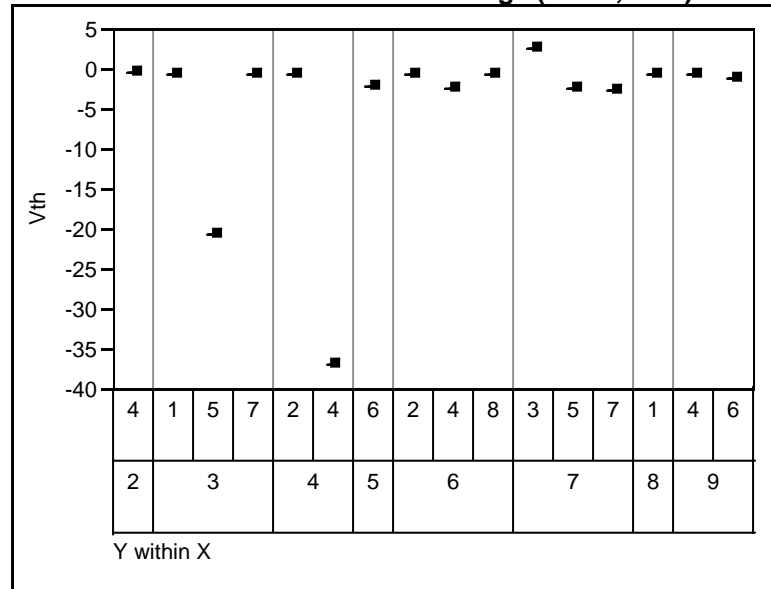
Wafer 5-PMOS Threshold Voltage (W=10,L=5)



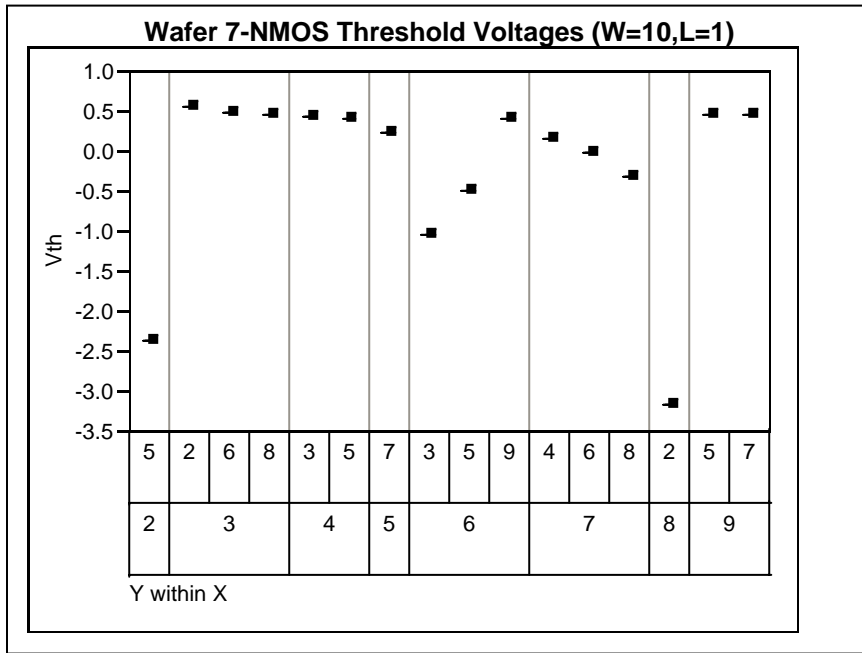
Wafer 5-PMOS Threshold Voltage (W=10,L=10)



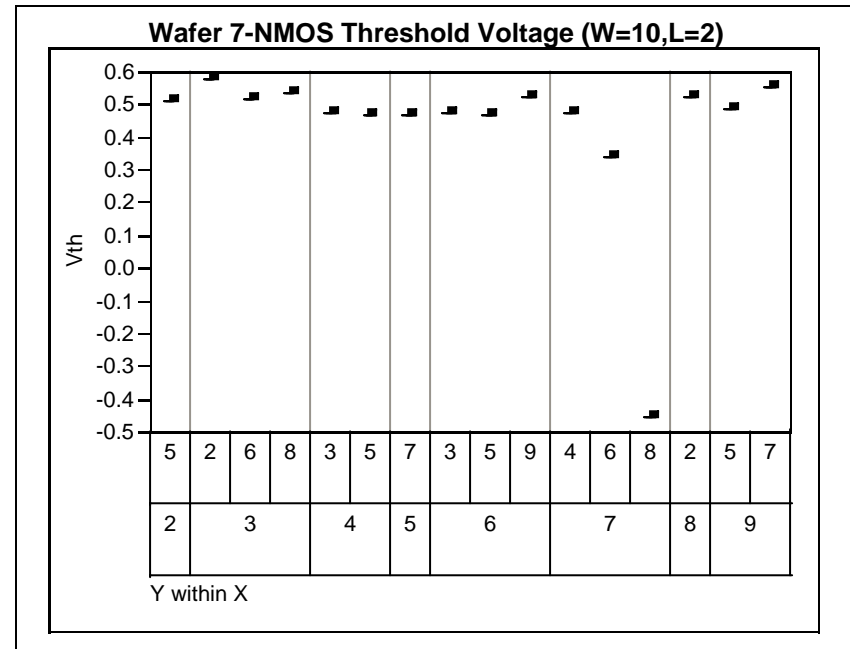
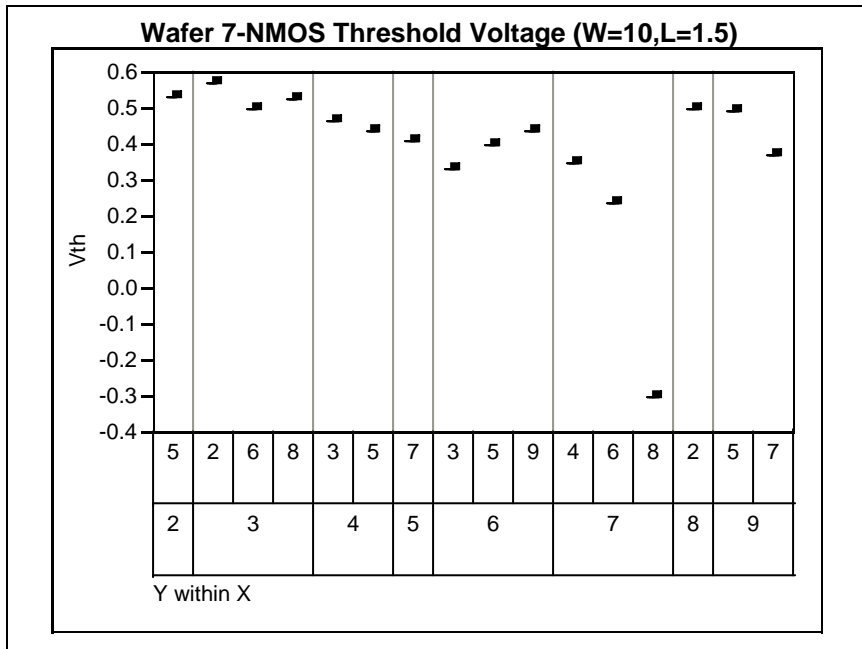
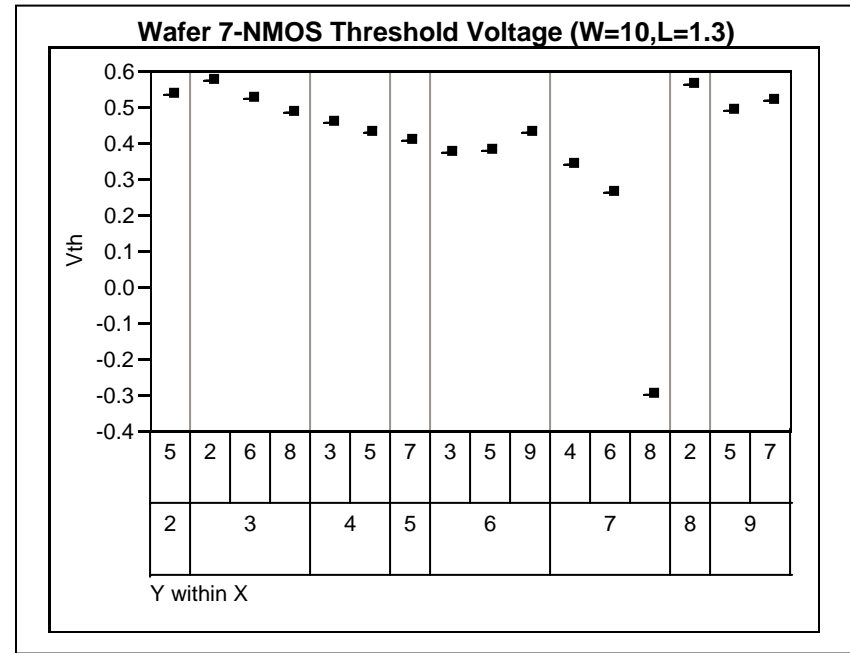
Wafer 5-PMOS Threshold Voltage (W=10,L=25)

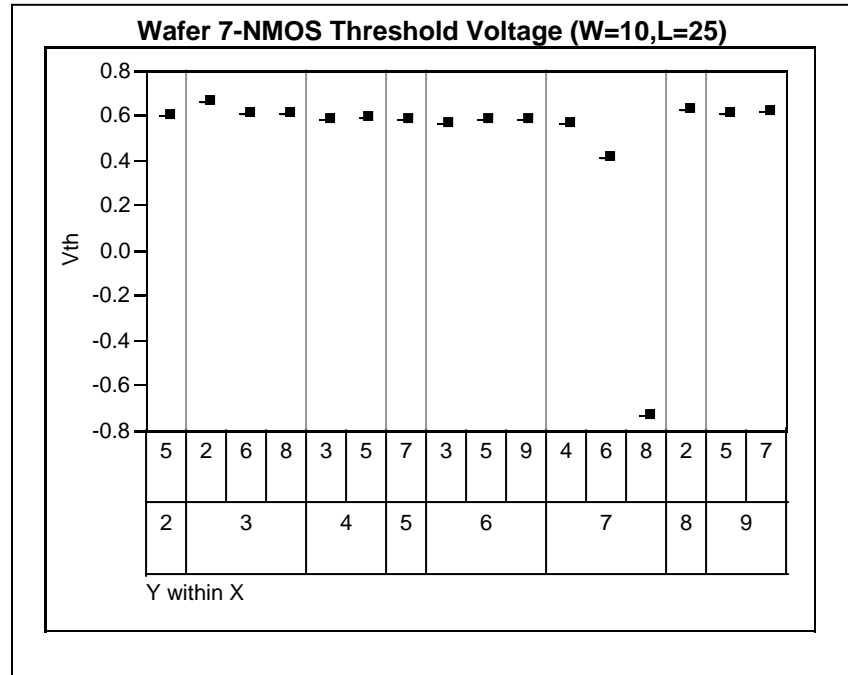
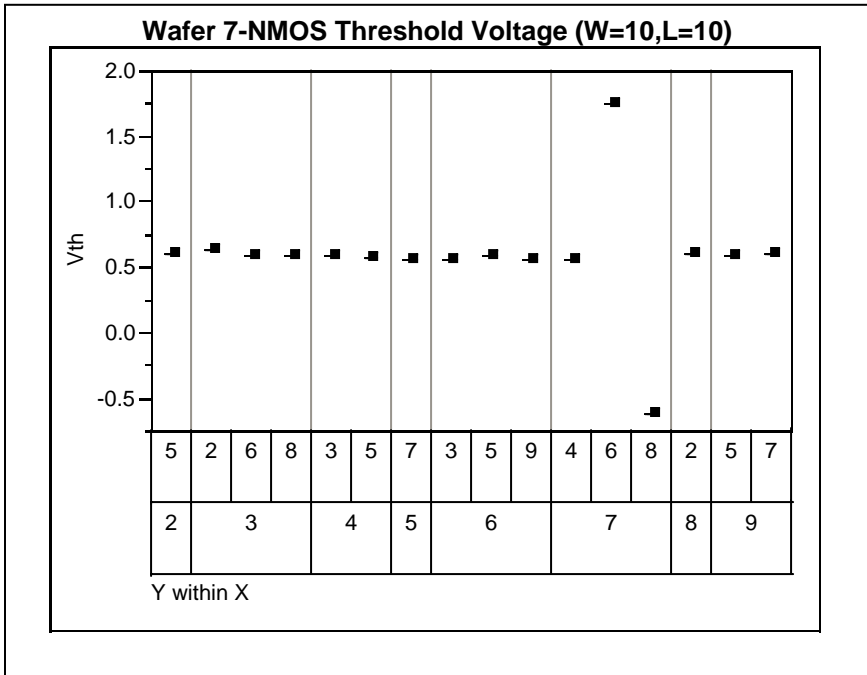
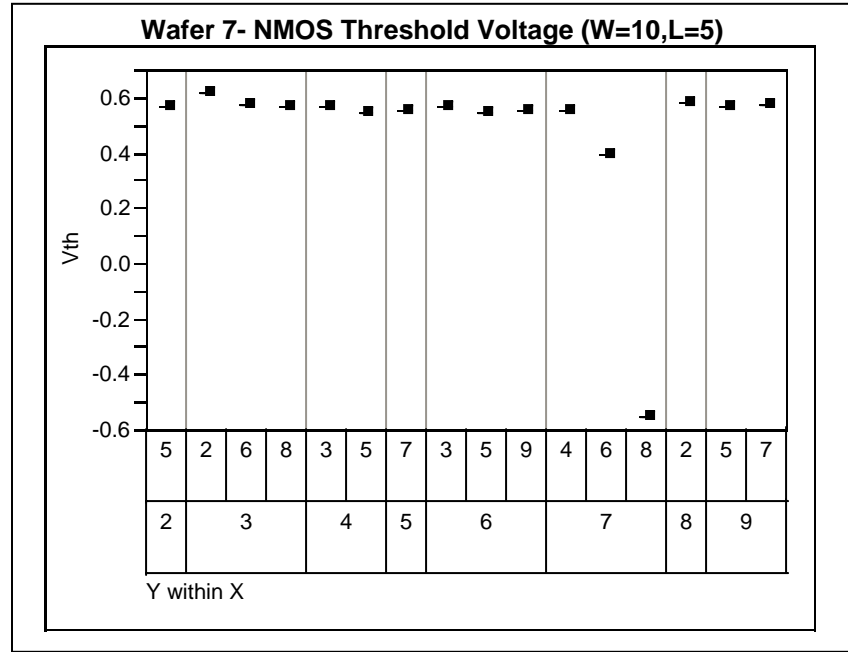
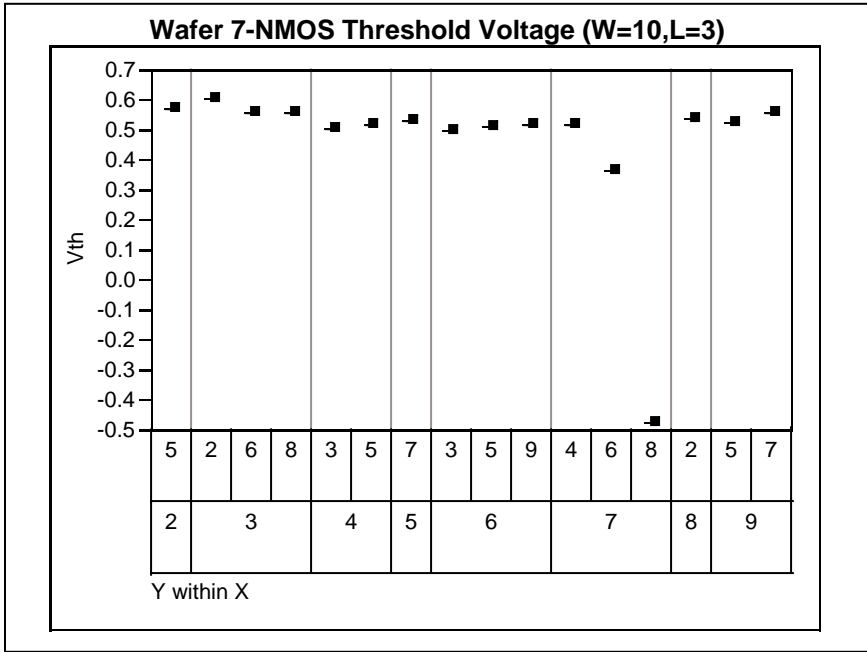


LCL=0,UCL=3V for NMOS.

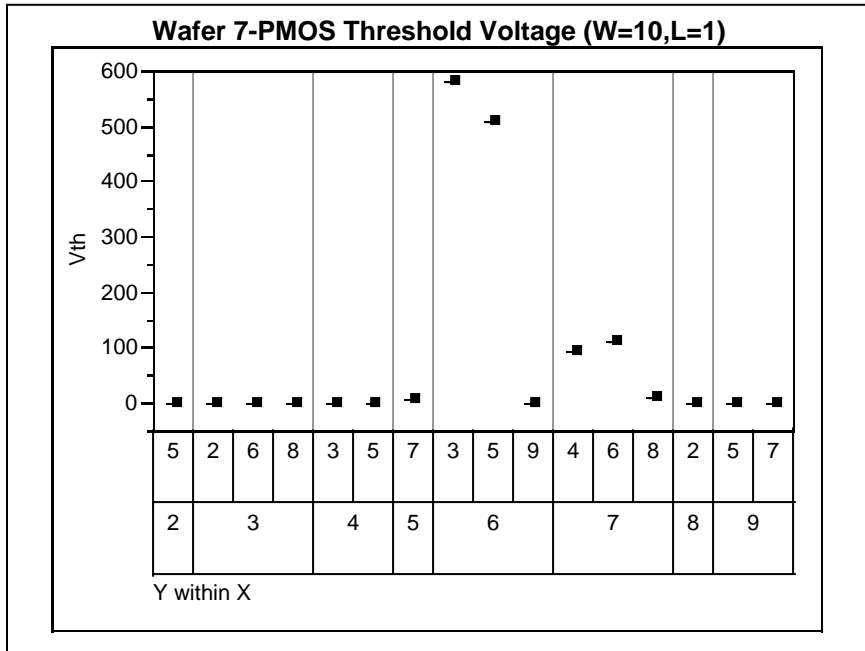


W=10, L=varied.

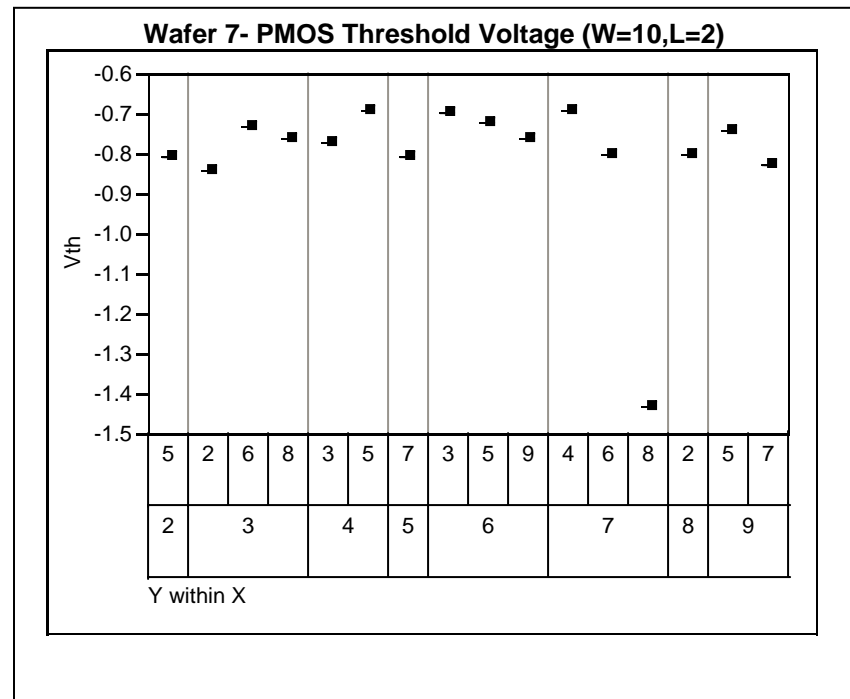
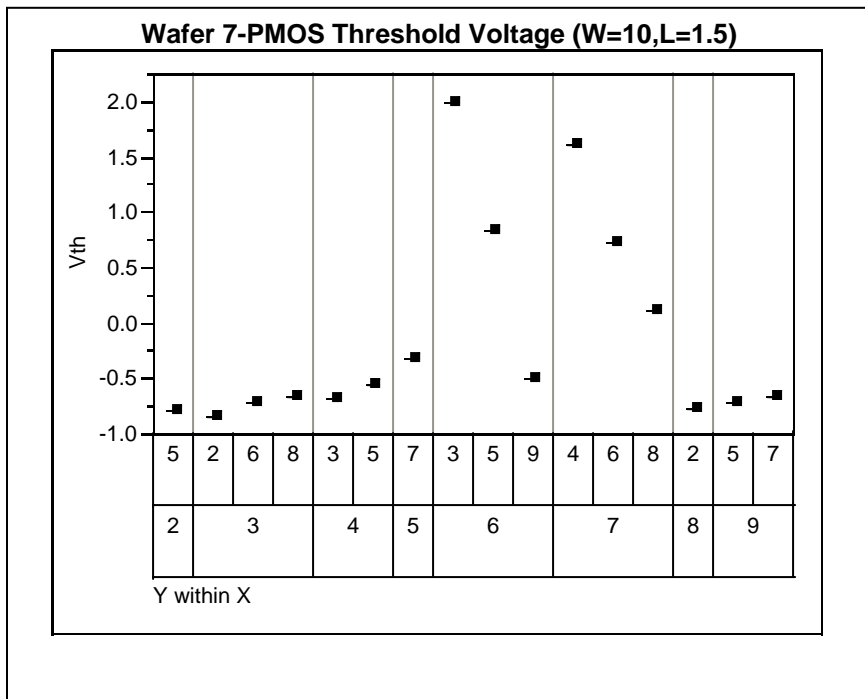
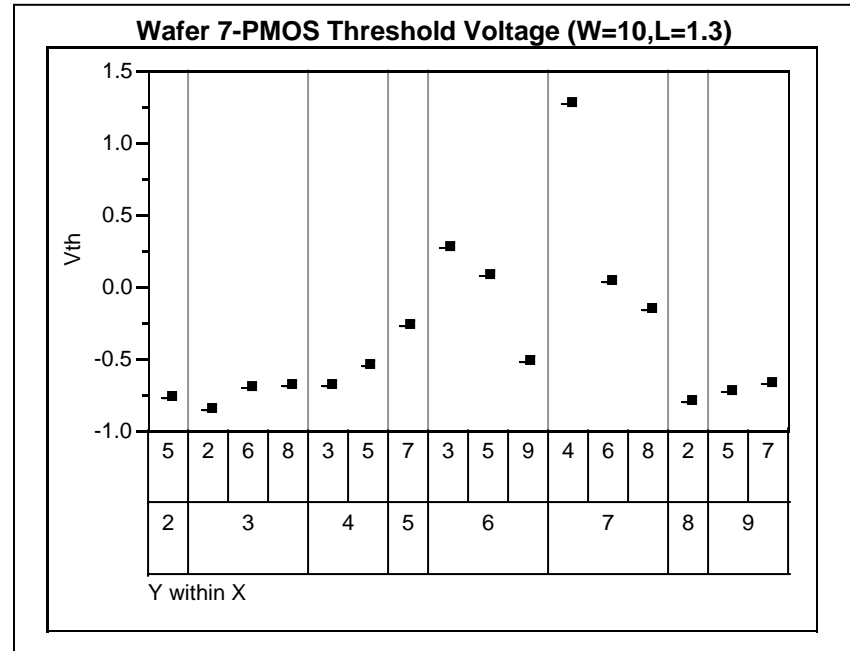




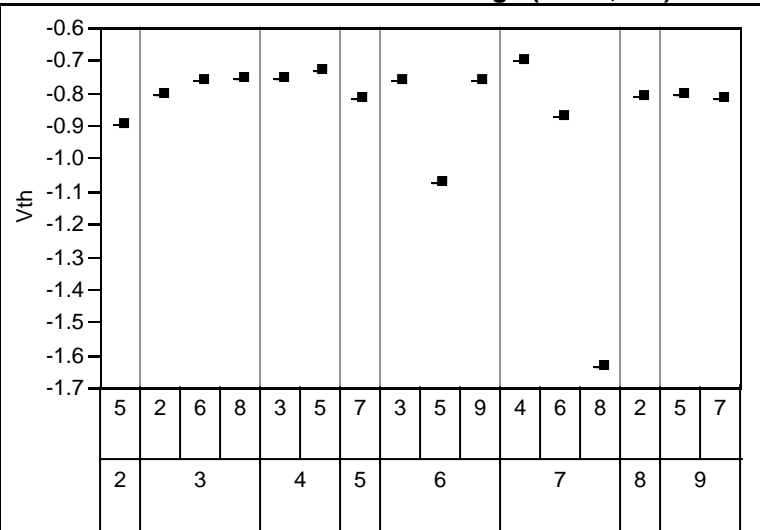
LCL= -3,UCL=0V for PMOS.



W=10, L=varied.

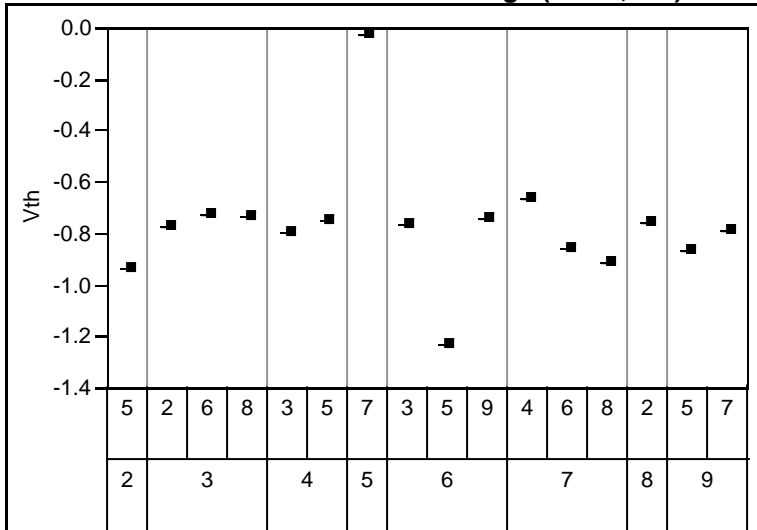


Wafer 7-PMOS Threshold Voltage (W=10,L=3)



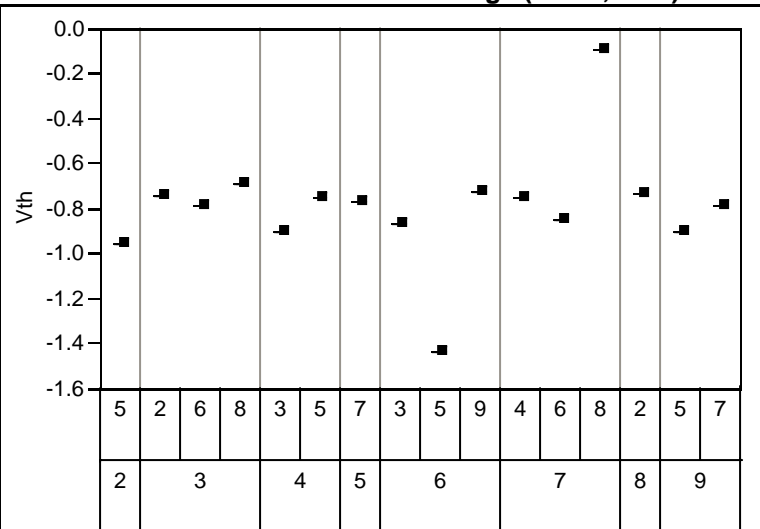
Y within X

Wafer 7- PMOS Threshold Voltage (W=10,L=5)



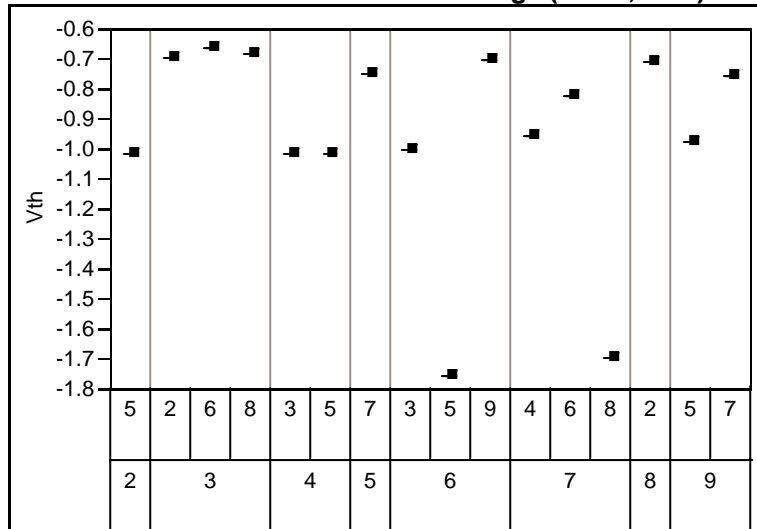
Y within X

Wafer 7-PMOS Threshold Voltage (W=10,L=10)



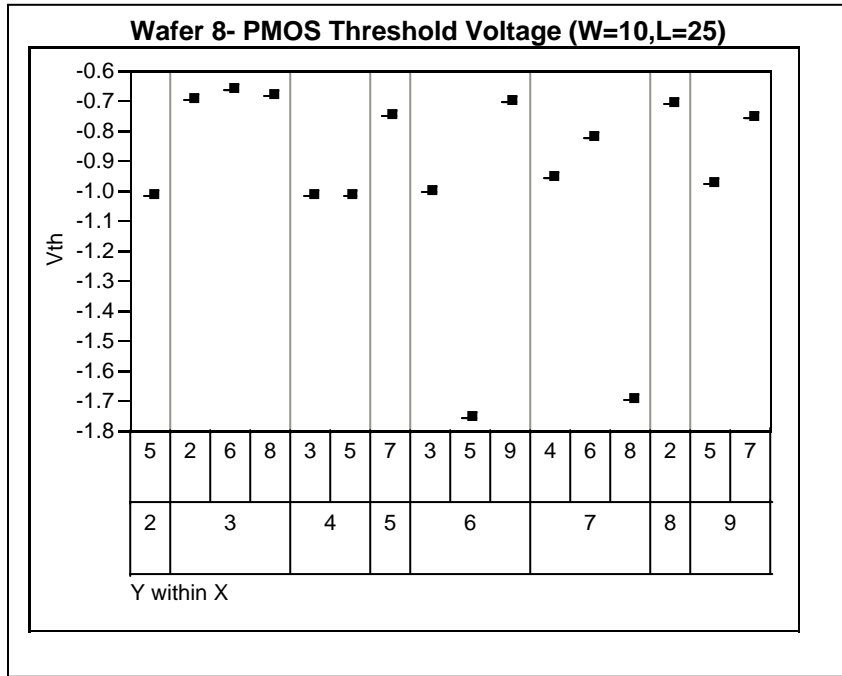
Y within X

Wafer 7- PMOS Threshold Voltage (W=10,L=25)

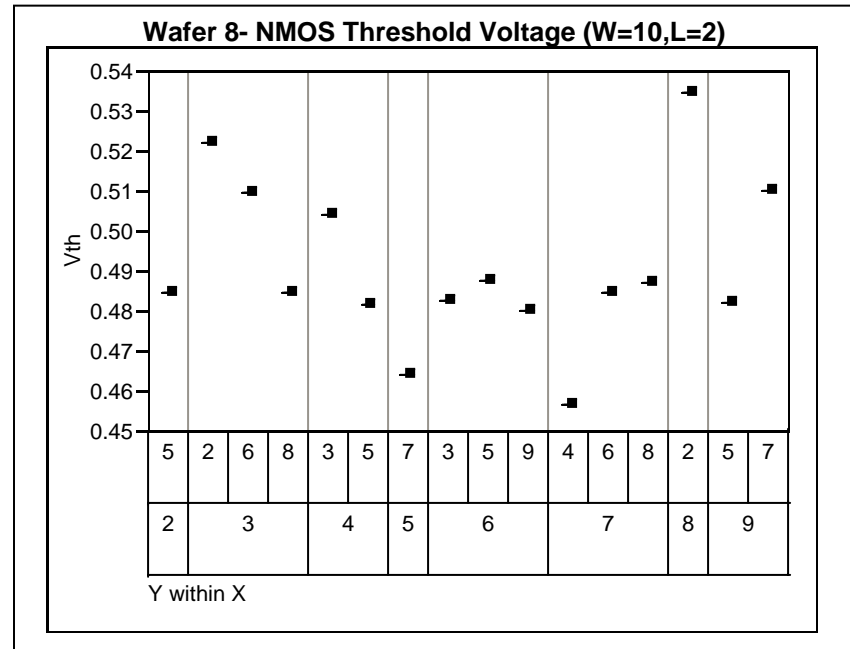
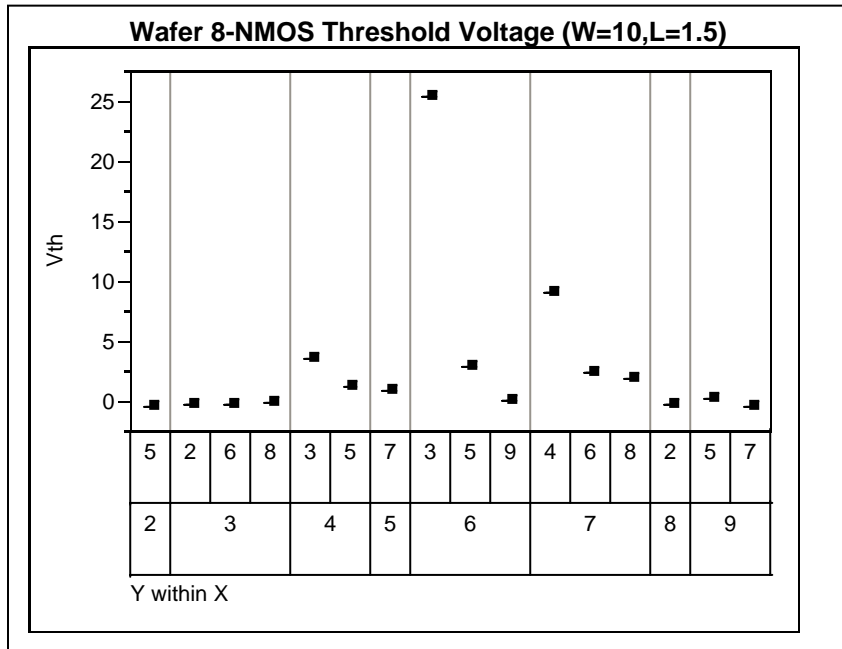
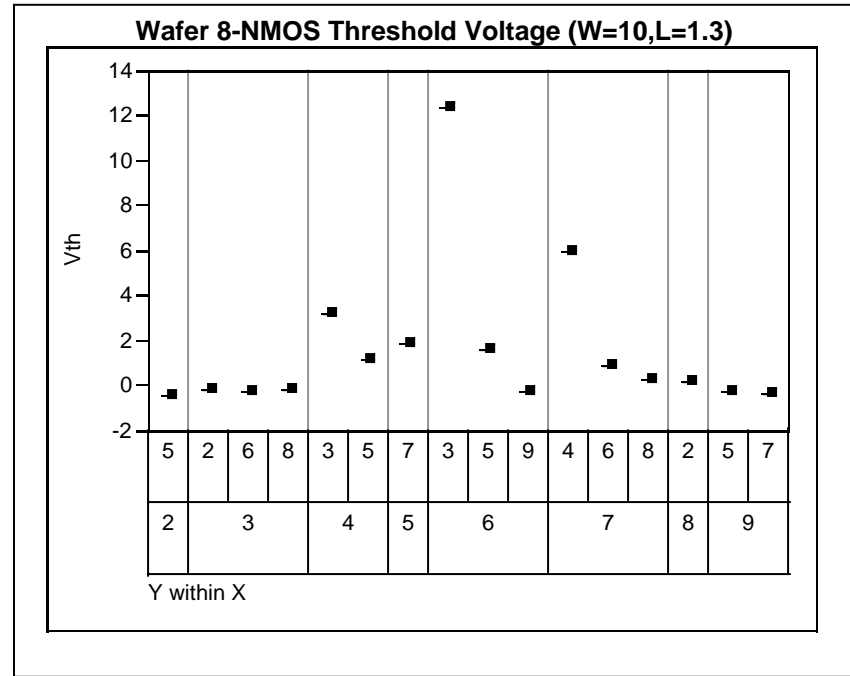


Y within X

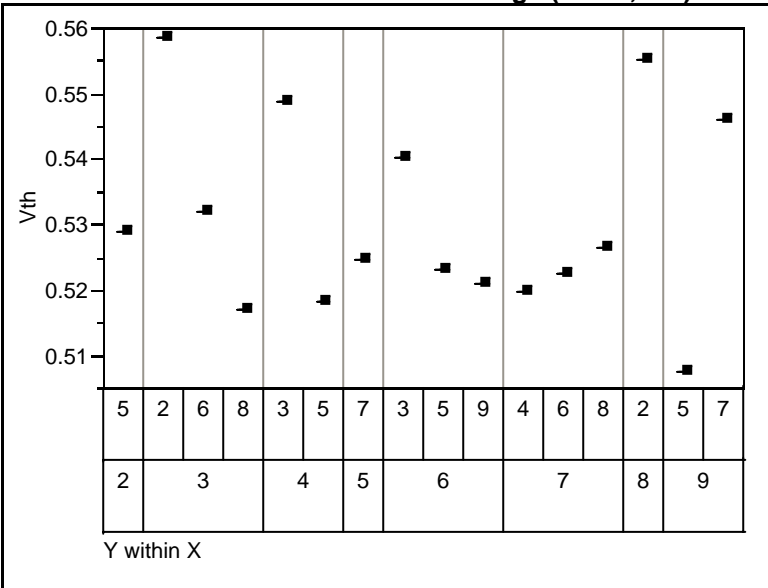
LCL=0,UCL=3V for NMOS.



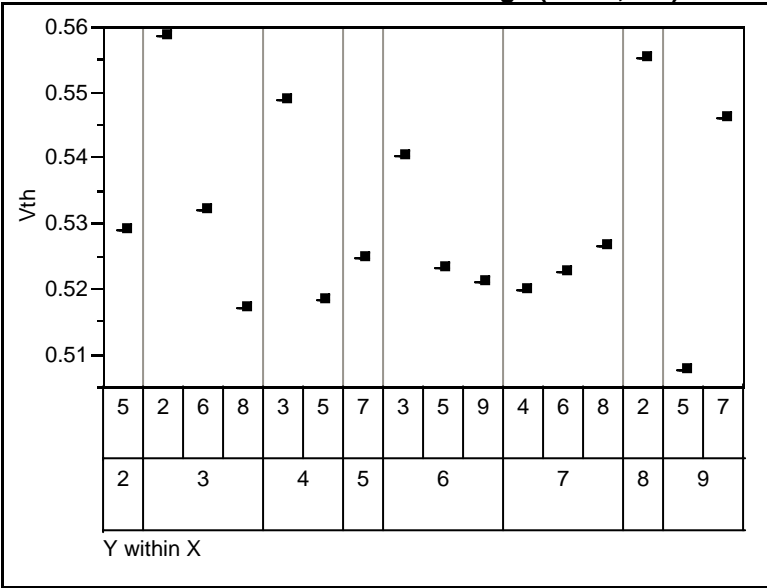
W=10, L=varied.



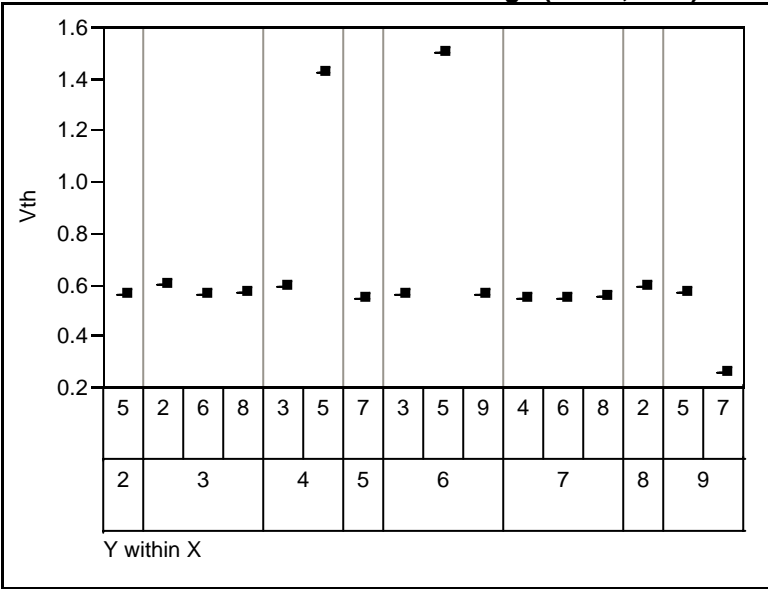
Wafer 8- NMOS Threshold Voltage (W=10,L=3)



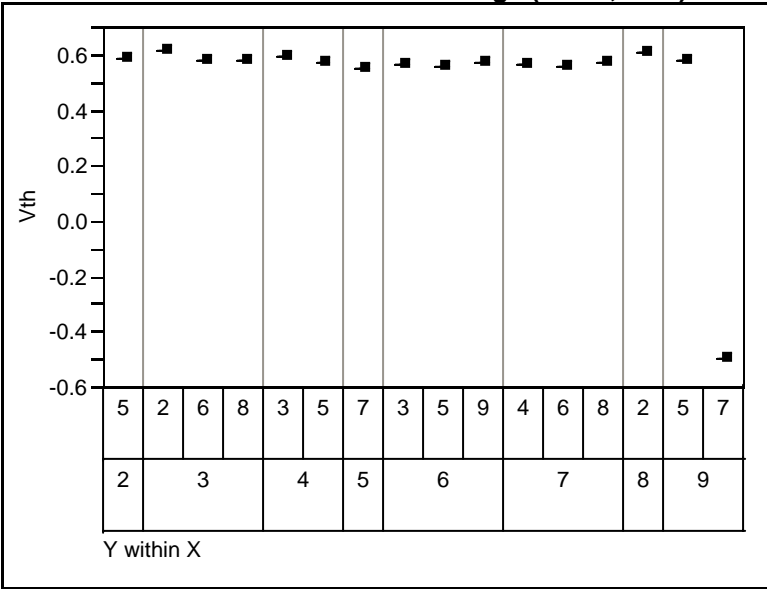
Wafer 8- NMOS Threshold Voltage (W=10,L=3)



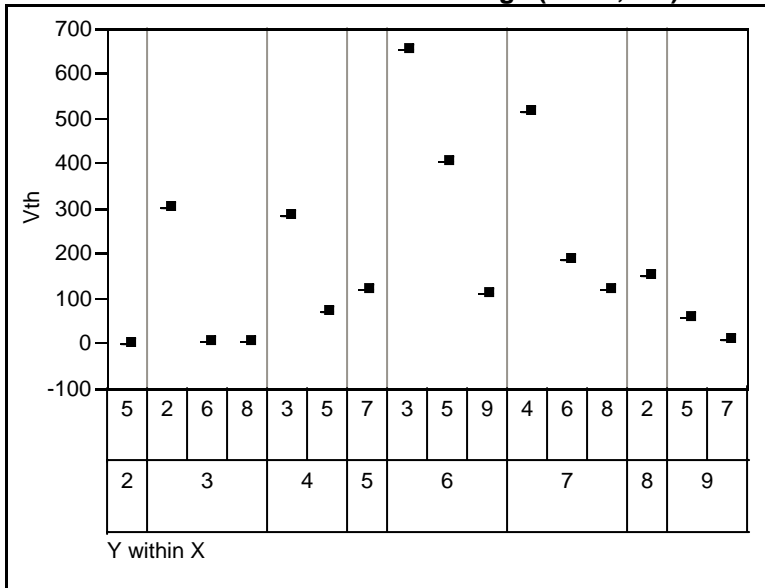
Wafer 8- NMOS Threshold Voltage (W=10,L=10)



Wafer 8- NMOS Threshold Voltage (W=10,L=25)

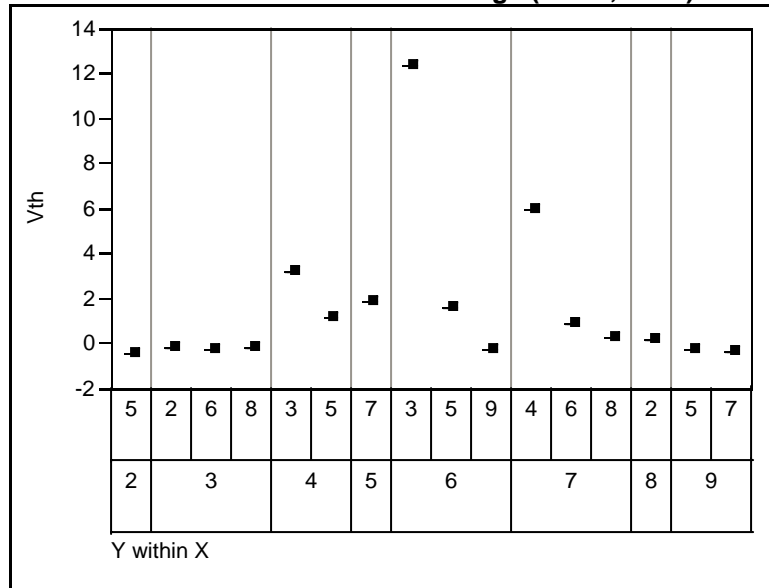


Wafer 8-PMOS Threshold Voltage (W=10,L=1)

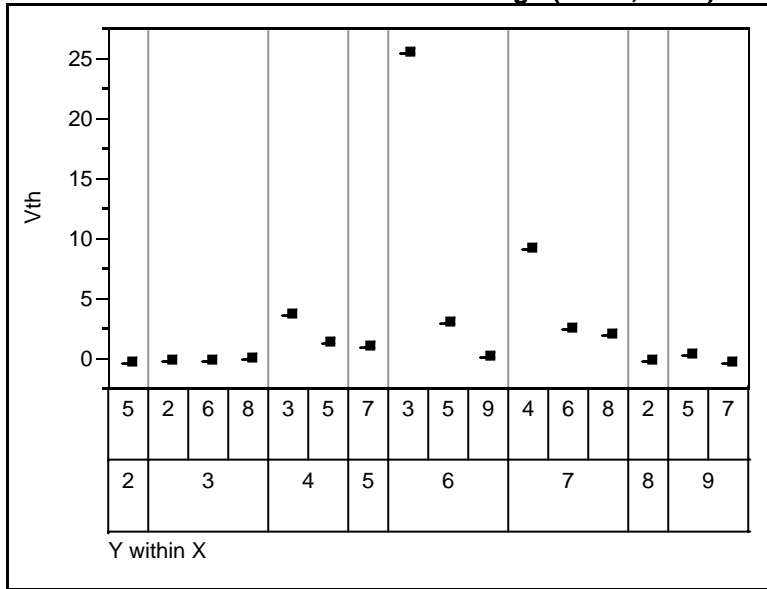


**Bad Transistor

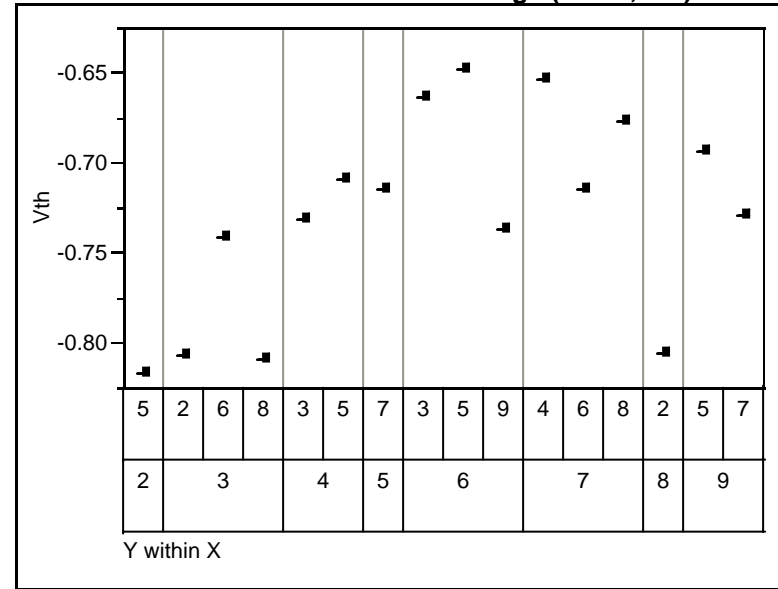
Wafer 8-PMOS Threshold Voltage (W=10,L=1.3)



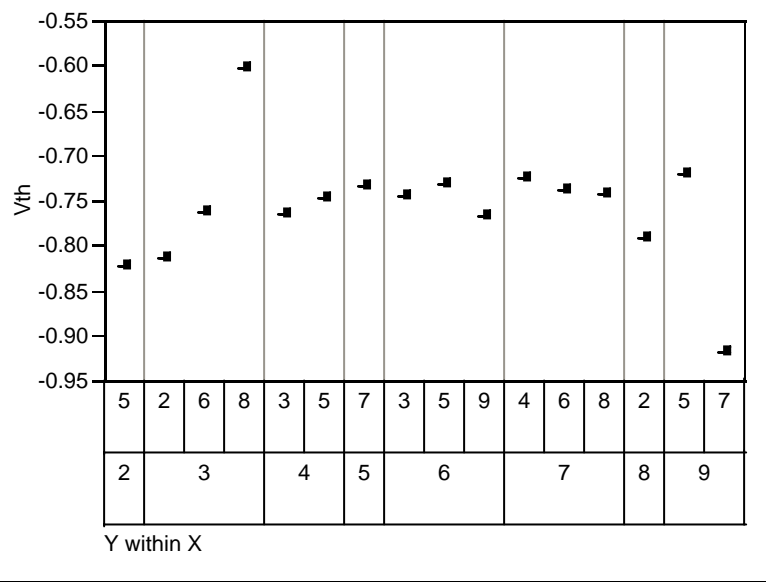
Wafer 8-PMOS Threshold Voltage (W=10,L=1.5)



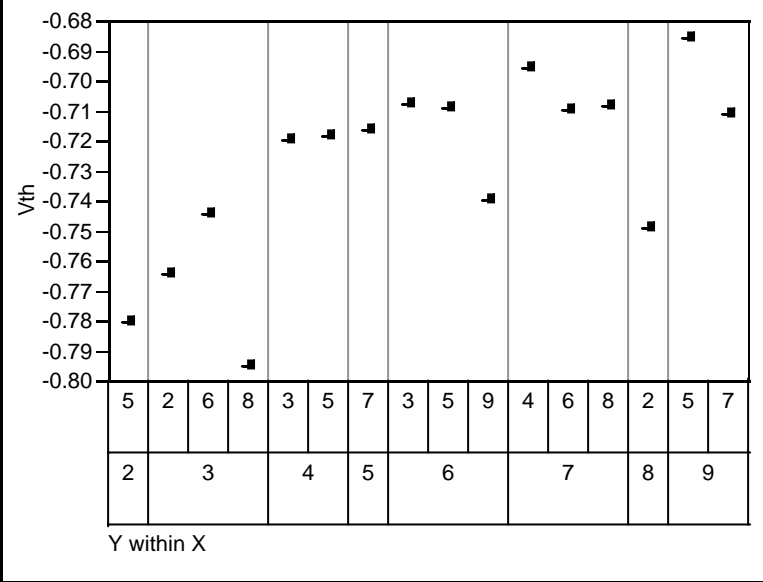
Wafer 8-PMOS Threshold Voltage (W=10,L=2)



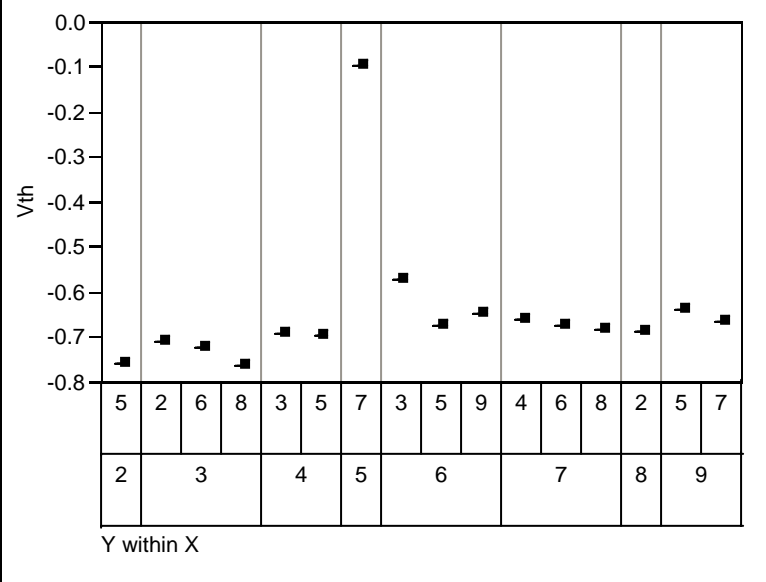
Wafer 8-PMOS Threshold Voltage (W=10,L=3)



Wafer 8-PMOS Threshold Voltage (W=10,L=5)



Wafer 8-PMOS Threshold Voltage (W=10,L=10)



Wafer 8-PMOS Threshold Voltage (W=10,L=25)

